3-7 Watt 48V-Input Isolated DC/DC Converter

(Revised 1/3/2002)



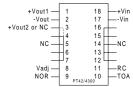
- Wide Input Voltage Range: 38V to 72V
- 83% Efficiency
- 1,500 VDC Isolation
- 18 Pin DIP Package
- 3.5 Million Hour MTBF
- Meets FCC/EN55022 Class A
- UL and CSA approved
- No External Components Required
- Adjustable Output Voltage

The PT4200 series of isolated DC/DC converters advance the state-of-the-art for board-mounted converters by employing

high switching frequencies, thick-film technology and a high degree of silicon integration. The high reliability and very low package height makes these converters ideal for Telecom and Datacom applications requiring input-to-output isolation with board spacing down to 0.6".

The PT4200 series is offered in a unique molded through-hole or SMD-DIP package with single output voltages of 2V, 3.3V, 5V, and 12V, dual outputs of $\pm 5V$, $\pm 5V/\pm 3.3V$, and $\pm 12V$.

Package (Top View)



Specifications

Characteristics (T _a = 25°C unless noted)	Symbols		PT42/4300 SERIES			
		Conditions	Min	Тур	Max	Units
Output Current	I_{o}	$\begin{array}{ll} Over V_{in} range & V_o = 2V, 3.3V \\ V_o = 5V \\ V_o = 12V \end{array}$	0 0 0	_	1.5 1.2 0.6	A A A
Current Limit	$I_{ m cl}$	$V_{in} = 48V \qquad V_{o} = 2V \\ V_{o} = 3.3V \\ V_{o} = 5V \\ V_{o} = 12V$	2.0 1.7 1.4 0.7		3.3 3.3 2.4 1.2	A A A
On/Off Standby Current	I _{in standby}	V _{in} = 48V, Pin 11 = -V _{in}	_	0.5	_	mA
Short Circuit Current	I_{sc}	$V_{in} = 48V$ $V_{o} = 2V$ $V_{o} = 3.3V$ $V_{o} = 5V$ $V_{o} = 12V$		2.8 2.4 1.9 1.2	_ _ _	A A A
Inrush Current	$\begin{matrix} I_{ir} \\ t_{ir} \end{matrix}$	$V_{\rm in}$ = 48V @ max $I_{\rm o}$ On start-up	_	0.6 1.0	1.0 5.0	A mSec
Input Voltage Range	V_{in}	Over I _o Range	38 (1)	48	72	V
Output Voltage Tolerance	$\Delta V_{ m o}$	Over Io Range	_	±4	_	%Vo
Idling Voltage	V_{o}	$I_o = 0A$ $V_o = 2V$ $V_o = 3.3V$ $V_o = 5V$ $V_o = 12V$	_ _ _	2.7 3.65 5.6 14.3	3.0 4.0 6.0 17	V V V
Ripple Rejection	RR	Over V _{in} range @ 120 Hz	_	60	_	dB
Line Regulation	Reg _{line}	Over V _{in} range @ max I _o	_	±0.5	_	$%V_{o}$
Load Regulation	Reg _{load}	10% to 100% of I_o max	_	±3	_	$%V_{o}$
Vo Ripple/Noise	V_n	V_{in} = 48 V , I_o = I_o max	_	30	70	$\mathrm{mV}_{\mathrm{pp}}$
Transient Response	t _{tr}	50% load change Vo over/undershoot	=	100 3.0	300 5.0	μSec %V _o
Efficiency	η	$\begin{array}{c} V_{in}\!\!=\!\!48V, I_o\!\!=\!\!1.5A, V_o\!\!=\!\!2V \\ V_{in}\!\!=\!\!48V, I_o\!\!=\!\!1.5A, V_o\!\!=\!\!3.3V \\ V_{in}\!\!=\!\!48V, I_o\!\!=\!\!1.2A, V_o\!\!=\!\!5V \\ V_{in}\!\!=\!\!48V, I_o\!\!=\!\!0.6A, V_o\!\!=\!\!12V \end{array}$	_ _ _	73 79 80 83	_ _ _	% % %
Switching Frequency	f_{o}	Over V _{in} and I _o	_	485	_	kHz
Operating Temperature	T_a	V _{in} = 48V @ max I _o Free air convection, (40-60LFM)	-40	_	+85	°C
Pin Temperature	Тр	@ Pin1	_	_	95	°C
Storage Temperature	T_s	_	-55	_	+125	°C
Mechanical Shock	_	Per Mil-STD-202F, Method 213B, 6mS half-sine, mounted to a PCB	_	50	_	G's
Mechanical Vibration	_	Per Mil-STD-202F, Method 204D, 10-500Hz, mounted to a PCB	_	10	_	G's
Weight	_	_	_	20	_	gram
Isolation	_	_	1500		_	VDC
Flammability		Materials meet UL 94V-0				

Notes: (1) The minimum input voltage is adjustable. See the specific application note on the PT4200/4205/4300 Series.

Pin-Out Information

Pin	Function		
1	$V_{out}1$		
2	V _{out} return		
3	Vout2 or N/C		
4	Do not connect		
5	Do not connect		
6	Do not connect		
7	Do not connect		
8*	V_{adj}		
9*	Nominal output voltage resistor		
10	Turn-on/offinput voltage adjust		
11	Remote on/off		
12	Do not connect		
13	Do not connect		
14	Do not connect		
15	Do not connect		
16	Do not connect		
17	-V _{in}		
18	+Vin		
* DI			

^{*} Please note that when the Vout adjust is not used, pin 8 must be connected to pin 9.

Ordering Information

Through-Hole

PT4201A = 2V/1.5A **PT4202A** = 3.3V/1.5A

PT4203A = 5V/1.2A **PT4204A** = 12V/0.6A

PT4301A = $\pm 5V/1A$ **PT4302A** = $\pm 5.2V/1A$,

+3.3V/1A**PT4303A** = $\pm 12V/0.25A$

Surface Mount

PT4201C = 2V/1.5APT4202C = 3.3V/1.5A

PT4203C = 5V/1.2APT4204C = 12V/0.6A

PT4301C = ± 5 V/1A

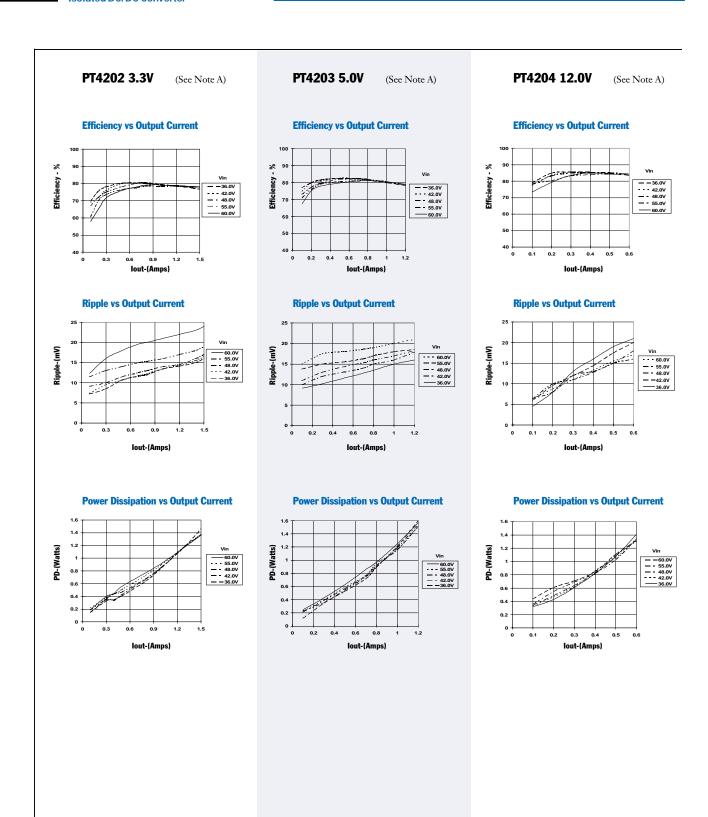
PT4302C = +5.2V/1A, +3.3V/1A

PT4303C = $\pm 12 \text{V}/0.25 \text{A}$

(For dimensions and PC board layout, see Package Style 900.)



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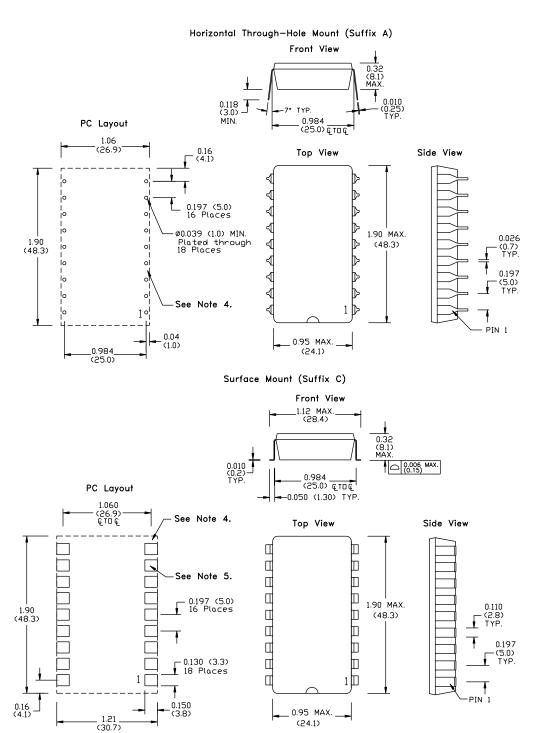
Note A: All data listed in the above graphs, except for derating data, has been developed from actual products tested at 25°C. This data is considered typical data for the DC-DC Converter.



Suffix A, C

(Revised 6/30/2000)

PACKAGE INFORMATION AND DIMENSIONS



Notes: (Rev. A)

- All dimensions are in inches (mm).
 2 place decimals are ±.030 (±0.8mm).
 3 place decimals are ±.010 (±0.3mm).
 4 Recommnended mechanical keep out area.
 Power pin connections should utilize two or more vias per input, ground and output pin.



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