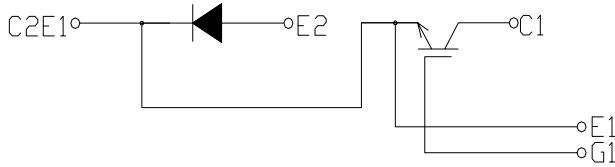
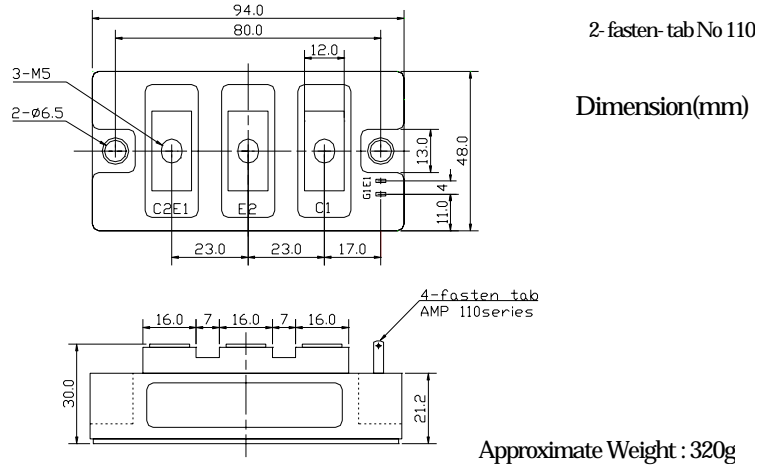


CIRCUIT



OUTLINE DRAWING



MAXMUM RATINGS (Tc=25°C)

Item	Symbol	PCHMB200A6	Unit
Collector-Emitter Voltage	V _{CES}	600	V
Gate - Emitter Voltage	V _{GES}	+/- 20	V
Collector Current	I _C	DC	200
		1 ms	400
Collector Power Dissipation	P _C	780	W
Junction Temperature Range	T _j	-40 to +150	°C
Storage Temperature Range	T _{sg}	-40 to +125	°C
Isolation Voltage Terminal to Base AC, 1 min.)	V _{ISO}	2500	V
Mounting Torque	F _{TOR}	Module Base to Heat sink	3.06
		Bus Bar to Main Terminals	2.04

ELECTRICAL CHARACTERISTICS (Tc=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit	
Collector-Emitter Cut-Off Current	I _{CES}	V _{CE} =600V, V _{GE} =0V	-	-	2.0	mA	
Gate-Emitter Leakage Current	I _{GES}	V _{GE} =+/- 20V, V _{CE} =0V	-	-	1.0	μA	
Collector-Emitter Saturation Voltage	V _{CE(sat)}	I _C =200A, V _{GE} =15V	-	2.1	2.6	V	
Gate-Emitter Threshold Voltage	V _{GE(th)}	V _{CE} =5V, I _C =200mA	4.0	-	8.0	V	
Input Capacitance	C _{ies}	V _{CE} =10V, V _{GE} =0V, f=1MHz	-	20000	-	pF	
Switching Time	Rise Time	t _r	V _{CC} = 300V	-	0.15	0.3	μs
	Turn-on Time	t _{on}	R _L = 3 ohm	-	0.25	0.4	
	Fall Time	t _f	R _G = 3.6 ohm	-	0.2	0.35	
	Turn-off Time	t _{off}	V _{GE} = +/- 15V	-	0.45	0.7	

FREE WHEELING DIODES RATINGS & CHARACTERISTICS (Tc=25°C)

Item	Symbol	Rated Value	Unit
Forward Current	DC	I _F	200
	1 ms	I _{FM}	400

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Peak Forward Voltage	V _F	I _F =200A, V _{GE} =0V	-	1.9	2.4	V
Reverse Recovery Time	t _{rr}	I _F =200A, V _{GE} =-10V, di/dt=200A/μs	-	0.15	0.25	μs

THERMAL CHARACTERISTICS

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Thermal Impedance	R _{th(j-c)}	Junction to Case	-	-	0.16	°C/W
			-	-	0.38	

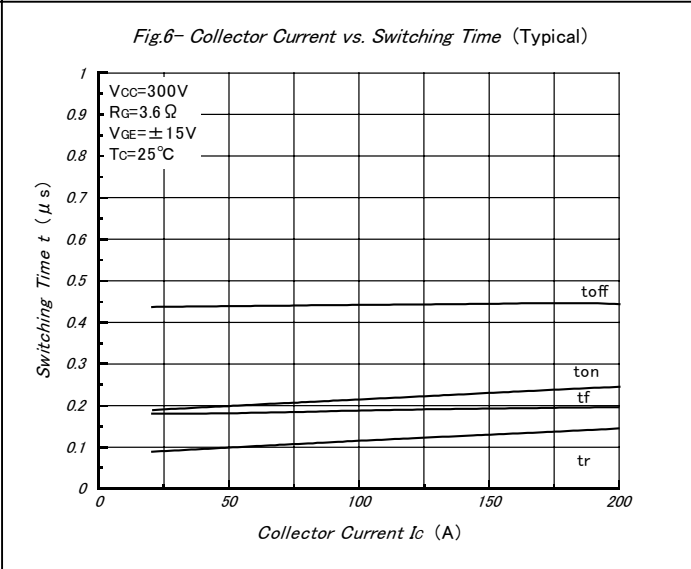
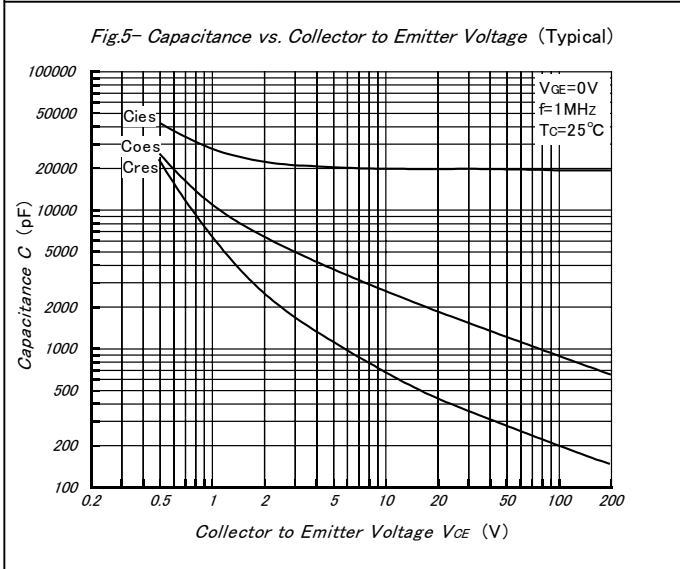
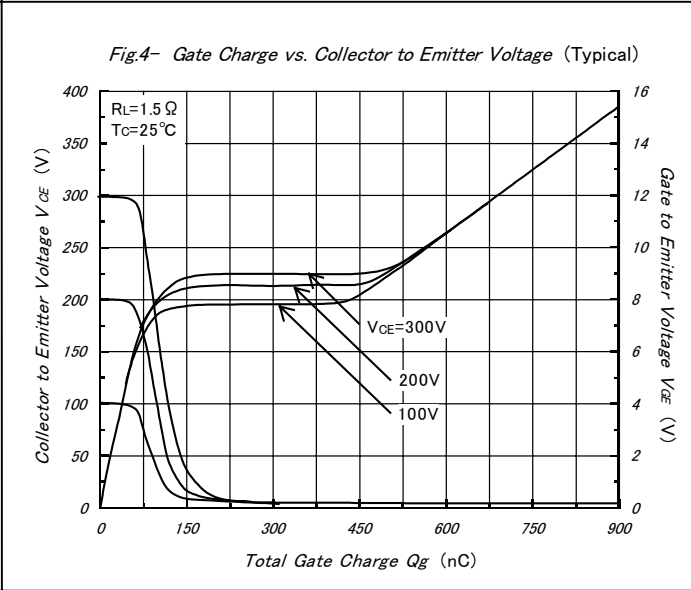
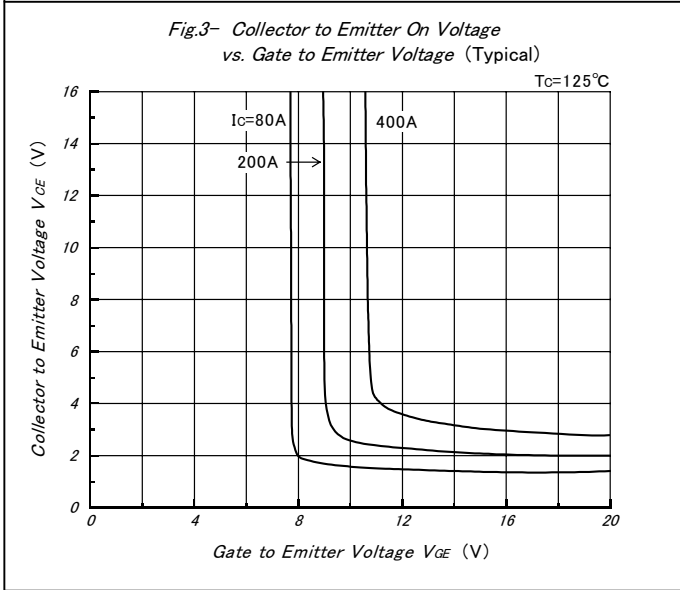
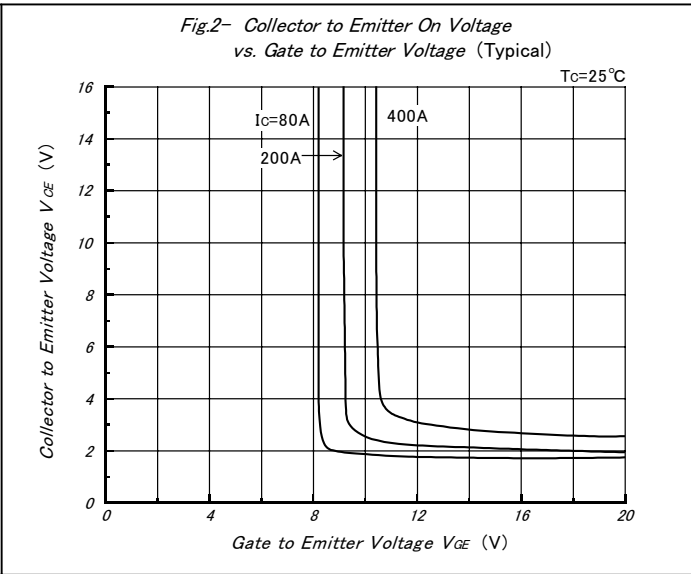
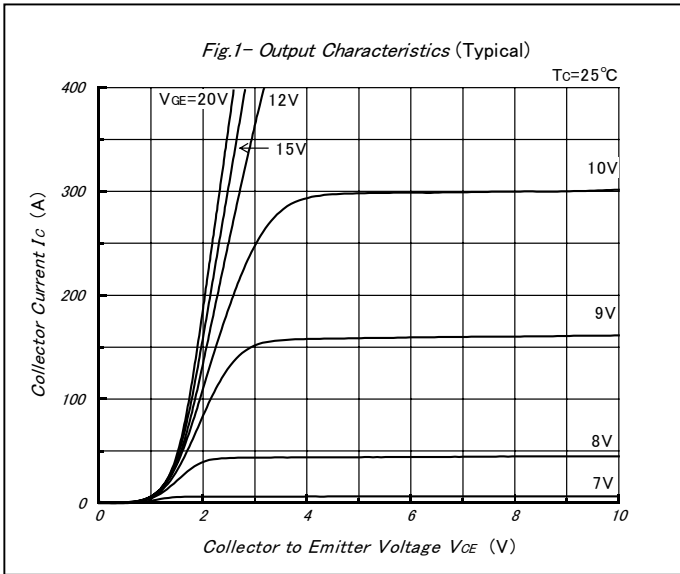


Fig.7- Series Gate Impedance vs. Switching Time (Typical)

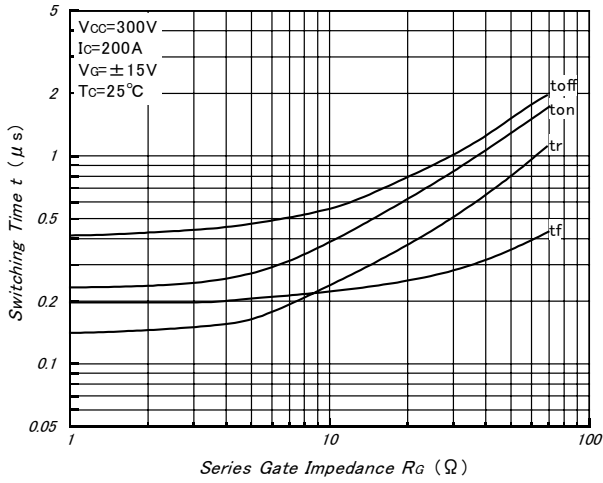


Fig.8- Forward Characteristics of Free Wheeling Diode (Typical)

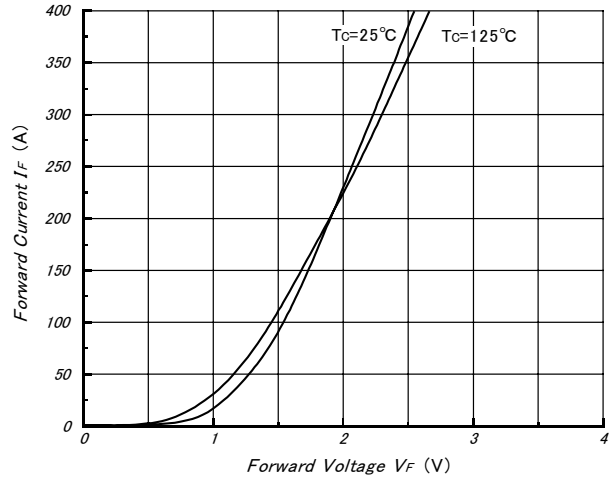


Fig.9- Reverse Recovery Characteristics (Typical)

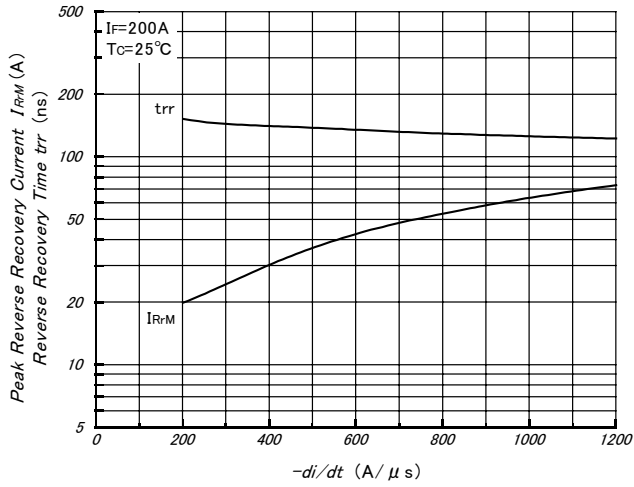


Fig.10- Reverse Bias Safe Operating Area (Typical)

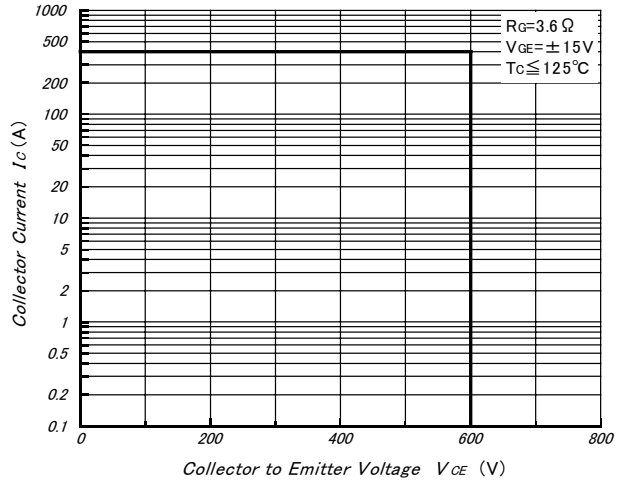


Fig.11- Transient Thermal Impedance

