

MG50M2CK1 Transistors

Darlington Transistor Array

Number of Devices 2

Type (NPN/PNP)

$V_{(BR)CEO}$ (V)

$V_{(BR)CBO}$ (V)

$I_{(C)}$ Max. (A)

$P_{(D)}$ Max. (W) 350

Minimum Operating Temp ($\circ C$)

Maximum Operating Temp ($\circ C$)

$I_{(CBO)}$ Max. (A)

@ $V_{(CBO)}$ (V) (Test Condition)

$h_{(FE)}$ Min. Current gain. 100

$h_{(FE)}$ Max. Current gain.

@ $I_{(C)}$ (A) (Test Condition) 25

@ $V_{(CE)}$ (V) (Test Condition) 5.0k

$f_{(T)}$ Min. (Hz) Transition Freq

@ $I_{(C)}$ (A) (Test Condition)

@ $V_{(CE)}$ (V) (Test Condition)

Emitter-Base Diode (Y/N)

Semiconductor Material

Package Style N/A

Mounting Style -