

MG25M2CK1 Transistors

Darlington Transistor Array

Number of Devices 2

Type (NPN/PNP)

V(BR)CEO (V)

V(BR)CBO (V)

I(C) Max. (A)

P(D) Max. (W) 300

Minimum Operating Temp (°C)

Maximum Operating Temp (°C)

I(CBO) Max. (A)

@V(CBO) (V) (Test Condition)

h(FE) Min. Current gain. 100

h(FE) Max. Current gain.

@I(C) (A) (Test Condition) 50

@V(CE) (V) (Test Condition) 5.0k

f(T) Min. (Hz) Transition Freq

@I(C) (A) (Test Condition)

@V(CE) (V) (Test Condition)

Emitter-Base Diode (Y/N)

Semiconductor Material

Package Style N/A

Mounting Style -