TOSHIBA GTR Module Silicon N Channel IGBT

MG200Q1JS40

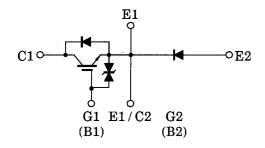
High Power Switching Applications Chopper Applications

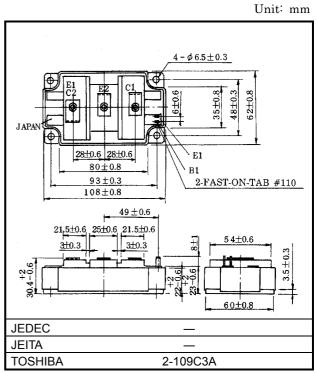
- High input impedance
- High speed: $t_f = 0.5 \mu s \text{ (max)}$

 $t_{rr} = 0.5 \mu s \text{ (max)}$

- Low saturation voltage
 - $V_{CE (sat)} = 4.0 V (max)$
- Enhancement-mode
- The electrodes are isolated from case.

Equivalent Circuit





Weight: 430g

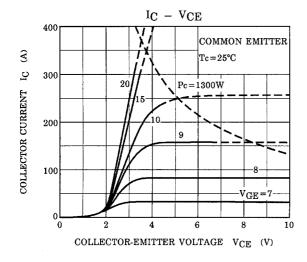
Maximum Ratings (Ta = 25°C)

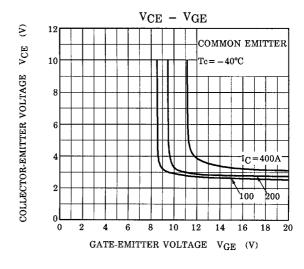
Characteristics		Symbol	Rating	Unit	
Collector-emitter voltage		V _{CES}	1200	V	
Gate-emitter voltage		V _{GES}	±20	V	
Reverse voltage		V _R	1200	V	
Collector current	DC	IC	200	Α	
	1ms	I _{CP}	400		
Forward current	DC	l _F	200	Α	
	1ms	I _{FM}	400		
Collector power dissipation (Tc = 25°C)		PC	1300	W	
Junction temperature		Tj	150	°C	
Storage temperature range		T _{stg}	-40 ~ 125	°C	
Isolation voltage		V _{Isol}	2500 (AC 1 min.)	V	
Screw torque (Terminal / mounting)		_	3/3	N·m	

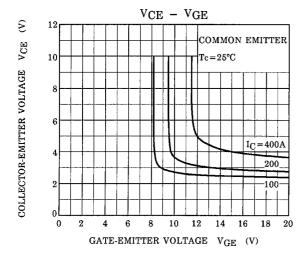
Electrical Characteristics (Ta = 25°C)

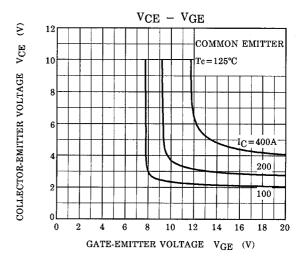
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GES}	V _{GE} = ±20V, V _{CE} = 0	_	_	±20	μΑ
Collector cut-off current		I _{CES}	V _{CE} = 1200V, V _{GE} = 0	_	_	2.0	mA
Gate-emitter cut-off voltage		V _{GE} (OFF)	V _{CE} = 5V, I _C = 200mA	3.0	_	6.0	V
Collector-emitter saturation voltage		V _{CE (sat)}	I _C = 200A, V _{GE} = 15V	_	3.0	4.0	V
Input capacitance		C _{ies}	V _{CE} = 10V, V _{GE} = 0 f = 1MHz	_	24000	_	pF
Switching time	Rise time	t _r	15V 4.7Ω Sc 600V	_	0.3	0.6	- µs
	Turn-on time	t _{on}		_	0.4	0.8	
	Fall time	t _f		_	0.2	0.5	
	Turn-off time	t _{off}		_	0.8	1.5	
Reverse current		I _R	V _R = 1200V	_	_	2.0	mA
Forward voltage		V _F	I _F = 200A, V _{GE} = 0	_	2.0	3.0	V
Reverse recovery time		t _{rr}	I _F = 200A, V _{GE} = -10V di / dt = 300A / μs	_	0.25	0.5	μs
Thermal resistance	Transistor	R _{th (j-c)}		_	_	0.096	°C / \\/
	Diode			_	_	0.25	°C/W

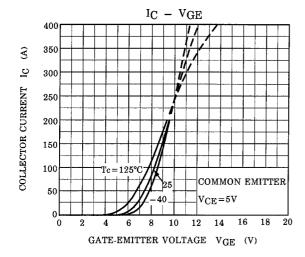
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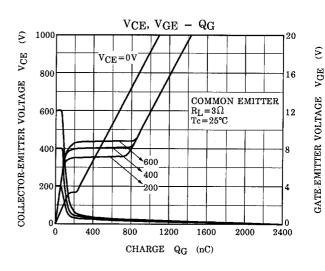




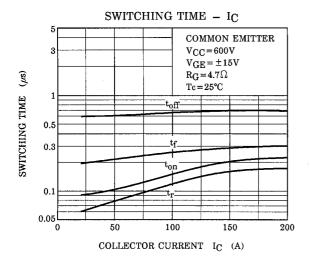


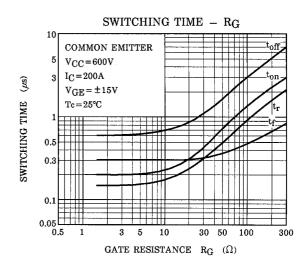


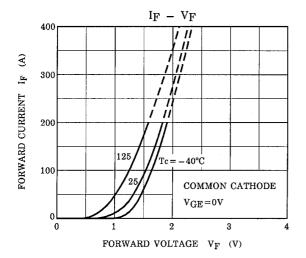


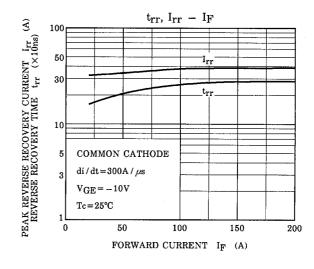


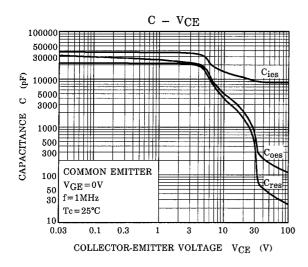
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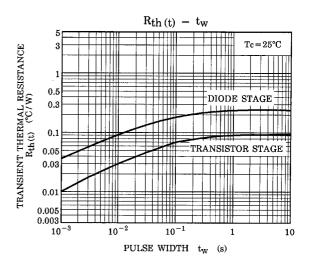




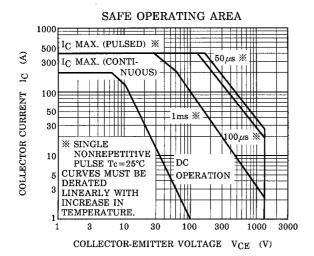


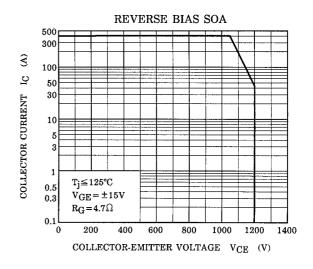






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