

PRELIMINARY
Notice: This is not a final specification.
Some parametric limits are subject to change.

MITSUBISHI IGBT MODULES

CM25AD05-24H

MEDIUM POWER SWITCHING USE
FLAT BASE, INSULATED TYPE

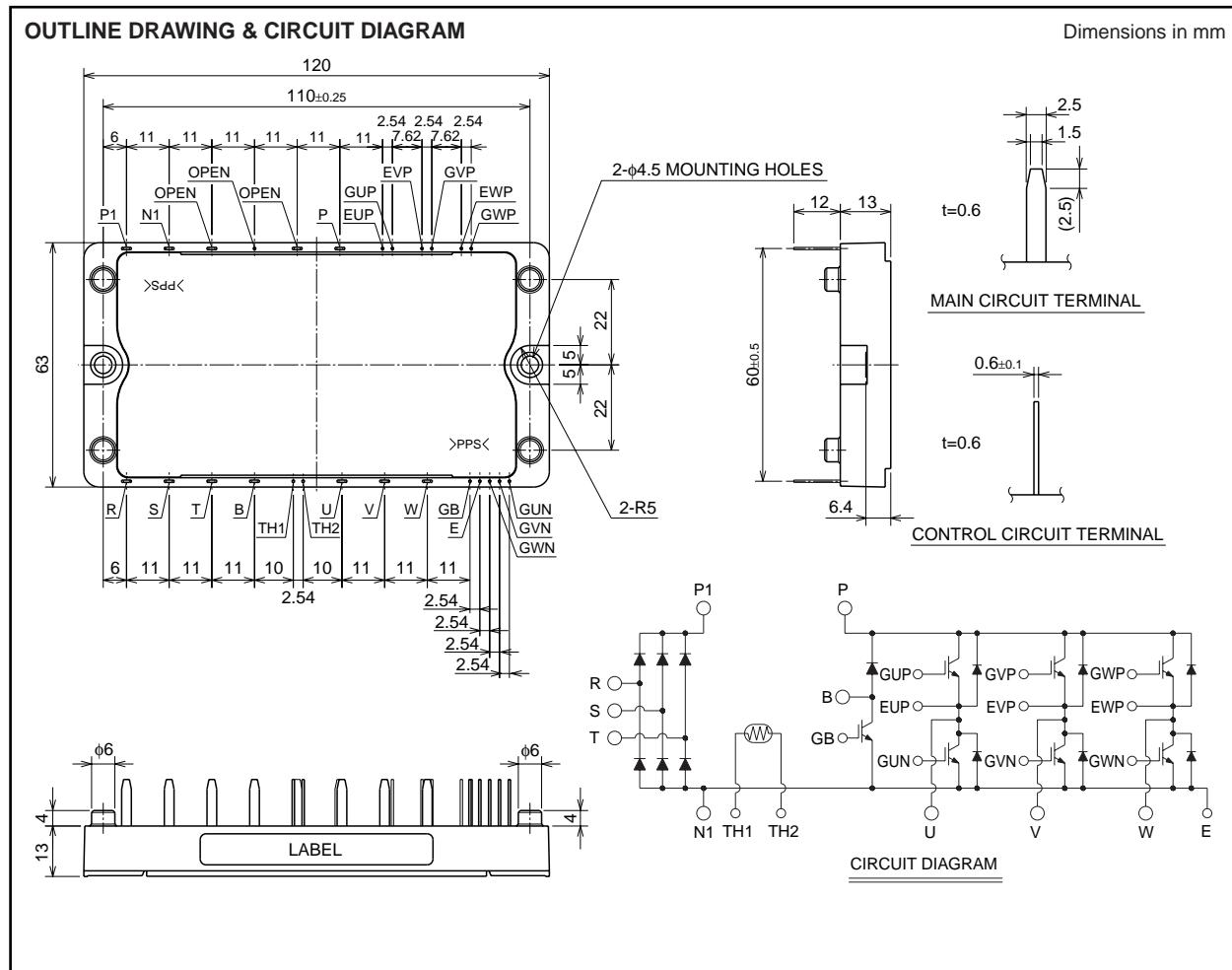
CM25AD05-24H



- IC 25A
- V_{CES} 1200V
- Insulated Type
- 3φ Inverter + 3φ Converter + Brake
+ Thermistor

APPLICATION

AC & DC motor controls, General purpose inverters



Aug. 1999

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MAXIMUM RATINGS ($T_J = 25^\circ\text{C}$)**INVERTER PART**

Symbol	Parameter	Conditions	Rating	Unit
V _{CES}	Collector-emitter voltage	G-E Short	1200	V
V _{GES}	Gate-emitter voltage	C-E Short	± 20	V
I _C	Collector Current	T _C = 25°C	25	A
I _{CM}		PULSE (Note. 2)	50	
I _E (Note.1)	Emitter Current	T _C = 25°C	25	A
I _{EM} (Note.1)		PULSE (Note. 2)	50	
P _C (Note.3)	Maximum collector dissipation	T _C = 25°C	—	W

BRAKE PART

Symbol	Parameter	Conditions	Rating	Unit
V _{CES}	Collector-emitter voltage	G-E Short	1200	V
V _{GES}	Gate-emitter voltage	C-E Short	± 20	V
I _C	Collector Current	T _C = 25°C	25	A
I _{CM}		PULSE (Note. 2)	50	
P _C (Note.3)	Maximum collector dissipation	T _C = 25°C	—	W
V _{RMM}	Repetitive peak reverse voltage	Clamp diode part	1200	V
I _{FM} (Note.3)	Forward current	Clamp diode part	25	A

CONVERTER PART

Symbol	Parameter	Conditions	Rating	Unit
V _{RMM}	Repetitive peak reverse voltage		1600	V
E _a	Recommended AC input voltage		440	V
I _O	DC output current	3φ rectifying circuit	25	A
I _{FSM}	Surge (non-repetitive) forward current	1/2 cycle at 60Hz, peak value, Non-repetitive	250	A
I ² t	I ² t for fusing	Value for one cycle of surge current	260	A ² s

COMMON RATING

Symbol	Parameter	Conditions	Rating	Unit
T _j	Junction temperature		-40 ~ +150	°C
T _{stg}	Storage temperature		-40 ~ +125	°C
V _{iso}	Isolation voltage	AC 1 min.	2500	V
—	Mounting torque	Mounting M4 screw	0.98 ~ 1.47	N·m
—	Weight	Typical value	140	g

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ELECTRICAL CHARACTERISTICS ($T_j = 25^\circ\text{C}$)**INVERTER PART**

Symbol	Parameter	Test conditions	Limits			Unit		
			Min.	Typ.	Max.			
I_{CES}	Collector cutoff current	$V_{CE} = V_{CES}$, $V_{GE} = 0\text{V}$	—	—	1	mA		
$V_{GE(\text{th})}$	Gate-emitter threshold voltage	$I_C = 2.5\text{mA}$, $V_{CE} = 10\text{V}$	4.5	6	7.5	V		
I_{GES}	Gate-emitter cutoff current	$V_{GE} = V_{GES}$, $V_{CE} = 0\text{V}$	—	—	0.5	μA		
$V_{CE(\text{sat})}$	Collector-emitter saturation voltage	$T_j = 25^\circ\text{C}$	$I_C = 25\text{A}$, $V_{GE} = 15\text{V}$	(Note.4)	—	2.7	3.4	V
		$T_j = 150^\circ\text{C}$			—	2.45	—	
C_{IES}	Input capacitance	$V_{CE} = 10\text{V}$ $V_{GE} = 0\text{V}$	—	—	5.0	nF		
C_{OES}	Output capacitance		—	—	3.8			
C_{RES}	Reverse transfer capacitance		—	—	1.0			
Q_G	Total gate charge	$V_{CC} = 600\text{V}$, $I_C = 25\text{A}$, $V_{GE} = 15\text{V}$	—	125	—	nC		
$t_{d(on)}$	Turn-on delay time	$V_{CC} = 600\text{V}$, $I_C = 25\text{A}$	—	—	100	ns		
t_r	Turn-on rise time	$V_{GE1} = V_{GE2} = 15\text{V}$	—	—	200			
$t_{d(off)}$	Turn-off delay time	$R_G = 13\Omega$	—	—	150			
t_f	Turn-off fall time	Resistive load	—	—	350			
$V_{EC(\text{Note.1})}$	Emitter-collector voltage	$I_E = 25\text{A}$, $V_{GE} = 0\text{V}$	—	—	3.5	V		
$t_{rr(\text{Note.1})}$	Reverse recovery time	$I_E = 25\text{A}$, $V_{GE} = 0\text{V}$	—	—	250	ns		
$Q_{rr(\text{Note.1})}$	Reverse recovery charge	$\text{die}/dt = -50\text{A}/\mu\text{s}$	—	0.22	—	μC		
$R_{th(j-c)Q}$	Thermal resistance	IGBT part, Per 1/6 module	—	—	—	$^\circ\text{C}/\text{W}$		
		FWDi part, Per 1/6 module	—	—	—			

BRAKE PART

Symbol	Parameter	Test conditions	Limits			Unit		
			Min.	Typ.	Max.			
I_{CES}	Collector cutoff current	$V_{CE} = V_{CES}$, $V_{GE} = 0\text{V}$	—	—	1	mA		
$V_{GE(\text{th})}$	Gate-emitter threshold voltage	$I_C = 2.5\text{mA}$, $V_{CE} = 10\text{V}$	4.5	6	7.5	V		
I_{GES}	Gate-emitter cutoff current	$V_{GE} = V_{GES}$, $V_{CE} = 0\text{V}$	—	—	0.5	μA		
$V_{CE(\text{sat})}$	Collector-emitter saturation voltage	$T_j = 25^\circ\text{C}$	$I_C = 25\text{A}$, $V_{GE} = 15\text{V}$	(Note.4)	—	2.7	3.4	V
		$T_j = 150^\circ\text{C}$			—	2.45	—	
C_{IES}	Input capacitance	$V_{CE} = 10\text{V}$ $V_{GE} = 0\text{V}$	—	—	5.0	nF		
C_{OES}	Output capacitance		—	—	3.8			
C_{RES}	Reverse transfer capacitance		—	—	1.0			
Q_G	Total gate charge	$V_{CC} = 600\text{V}$, $I_C = 25\text{A}$, $V_{GE} = 15\text{V}$	—	125	—	nC		
V_{FM}	Forward voltage drop	$I_F = 25\text{A}$, Clamp diode part	—	—	1.5	V		
$R_{th(j-c)Q}$	Thermal resistance	IGBT part	—	—	—	$^\circ\text{C}/\text{W}$		
		Clamp diode part	—	—	—			

CONVERTER PART

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
I_{RRM}	Repetitive reverse current	$VR = V_{RRM}$, $T_j = 150^\circ\text{C}$	—	—	8	mA
V_{FM}	Forward voltage drop	$I_F = 25\text{A}$	—	—	1.5	V
$R_{th(j-c)}$	Thermal resistance	Per 1/6 module	—	—	—	$^\circ\text{C}/\text{W}$

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THERMISTOR PART

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
R _{TH}	Resistance	T _C = 25°C	—	(100)	—	kΩ
B	B Constant	Resistance at 25°C, 50°C (Note.5)	—	(4000)	—	K

() : These parametric limits are tentative.

COMMON RATING

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
R _{Th(c-f)}	Contact thermal resistance	Case to fin, Thermal compound applied*1 (1 module)	—	—	—	°C/W

Note.1 IE, VEC, tr, Qrr, die/dt represent characteristics of the anti-parallel, emitter to collector free-wheel diode.

2 Pulse width and repetition rate should be such that the device junction temp. (T_j) does not exceed T_{jmax} rating.

3 Junction temperature (T_j) should not increase beyond 150°C.

4 Pulse width and repetition rate should be such as to cause negligible temperature rise.

5 B = (lnR₁-lnR₂)/(1/T₁-1/T₂)
 R₁ : Resistance at T₁(K)
 R₂ : Resistance at T₂(K)

*1 : Typical value is measured by using Shin-etsu Silicone "G-746".