

### Features

- Temperature protection provided by directly detecting the junction temperature of the IGBTs
- Low power loss and soft switching
- High performance and high reliability IGBT with overheating protection
- Higher reliability because of a big decrease in number of parts in built-in control circuit



### Maximum ratings and characteristics

- Absolute maximum ratings (at  $T_c=25^{\circ}\text{C}$  unless otherwise specified)

Item	Symbol	Rating		Unit		
		Min.	Max.			
Bus voltage	DC	$V_{DC}$	0	450	V	
	Surge	$V_{DC(surge)}$	0	500	V	
	Short operating	$V_{sc}$	200	400	V	
Collector-Emitter voltage *1		$V_{CES}$	0	600	V	
Inverter	Collector current	DC	$I_C$	-	75	A
		1ms	$I_{CP}$	-	150	A
		Duty=75.0% *2	$-I_C$	-	75	A
Collector power dissipation	One transistor *3	$P_C$	-	198	W	
	Collector current	DC	$I_C$	-	50	A
		1ms	$I_{CP}$	-	100	A
	Forward current diode		$I_F$	-	50	A
	Collector power dissipation	One transistor *3	$P_C$	-	198	W
Supply voltage of Pre-Driver *4		$V_{CC}$	-0.5	20	V	
Input signal voltage *5		$V_{in}$	-0.5	$V_{CC}+0.5$	V	
Input signal current		$I_{in}$	-	3	mA	
Alarm signal voltage *6		$V_{ALM}$	-0.5	$V_{CC}$	V	
Alarm signal current *7		$I_{ALM}$	-	20	mA	
Junction temperature		$T_j$	-	150	$^{\circ}\text{C}$	
Operating case temperature		$T_{opr}$	-20	100	$^{\circ}\text{C}$	
Storage temperature		$T_{stg}$	-40	125	$^{\circ}\text{C}$	
Solder temperature *8		$T_{sol}$	-	260	$^{\circ}\text{C}$	
Isolating voltage (Terminal to base, 50/60Hz sine wave 1min.)		$V_{iso}$	-	AC2500	V	
Screw torque	Mounting (M5)		-	3.5	N·m	

#### Note

\*1 :  $V_{ces}$  shall be applied to the input voltage between terminal P and U or ,u or W, N and U or V or W

\*2 :  $125^{\circ}\text{C}/\text{FWD } R_{th(j-c)} / (I_C \times V_F \text{ MAX}) = 125 / 0.855 / (75 \times 2.6) \times 100 = 75.0\%$

\*3 :  $P_C = 125^{\circ}\text{C}/\text{IGBT } R_{th(j-c)} = 125 / 0.63 = 198\text{W}$  [Inverter]

$P_C = 125^{\circ}\text{C}/\text{IGBT } R_{th(j-c)} = 125 / 0.63 = 198\text{W}$  [Breake]

\*4 :  $V_{CC}$  shall be applied to the input voltage between terminal No.4 and 1, 8 and 5, 12 and 9, 14 and 13

\*5 :  $V_{in}$  shall be applied to the input voltage between terminal No.3 and 1, 7 and 5, 11 and 9, 16,17,18 and 13.

\*6 :  $V_{ALM}$  shall be applied to the voltage between terminal No.2 and 1, No6 and 5, No10 and 9, No.19 and 13.

\*7 :  $I_{ALM}$  shall be applied to the input current to terminal No.2,6,10 and 19.

\*8 : Immersion time  $10 \pm 1\text{sec}$ .

Electrical characteristics (at T<sub>c</sub>=T<sub>j</sub>=25°C, V<sub>cc</sub>=15V unless otherwise specified.)

## ● Main circuit

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	
Inverter	Collector current at off signal input	ICES	V <sub>CE</sub> =600V V <sub>in</sub> terminal open.	-	-	1.0	mA	
	Collector-Emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>c</sub> =75A	Terminal	-	-	2.4	V
				Chip	-	2.0	-	
	Forward voltage of FWD	V <sub>F</sub>	-I <sub>c</sub> =75A	Terminal	-	-	2.6	V
Chip				-	1.6	-		
Brake	Collector current at off signal input	ICES	V <sub>CE</sub> =600V V <sub>in</sub> terminal open.	-	-	1.0	mA	
	Collector-Emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>c</sub> =50A	Terminal	-	-	2.2	V
				Chip	-	1.75	-	
	Forward voltage of Diode	V <sub>F</sub>	-I <sub>c</sub> =50A	Terminal	-	-	3.3	V
Chip				-	1.9	-		
Turn-on time	ton	V <sub>DC</sub> =300V, T <sub>j</sub> =125°C		1.2	-	-	μs	
Turn-off time	toff	I <sub>c</sub> =75A Fig.1, Fig.6		-	-	3.6		
Reverse recovery time	trr	V <sub>DC</sub> =300V, I <sub>c</sub> =75A Fig.1, Fig.6		-	-	0.3		
Maximum Avalanche Energy (A non-repetition)	P <sub>AV</sub>	Internal wiring inductance=50nH Main circuit wiring inductance=54nH		40	-	-	mJ	

## ● Control circuit

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply current of P-line side pre-driver(one unit)	I <sub>ccp</sub>	Switching Frequency : 0 to 15kHz T <sub>c</sub> =-20 to 125°C Fig.7	-	-	18	mA
Supply current of N-line side pre-driver	I <sub>ccn</sub>		-	-	65	mA
Input signal threshold voltage (on/off)	V <sub>in(th)</sub>	ON	1.00	1.35	1.70	V
		OFF	1.25	1.60	1.95	V
Input zener voltage	V <sub>Z</sub>	R <sub>in</sub> =20k ohm	-	8.0	-	V
Alarm signal hold time	t <sub>ALM</sub>	T <sub>c</sub> =-20°C Fig.2	1.1	-	-	ms
		T <sub>c</sub> =25°C Fig.2	-	2.0	-	ms
		T <sub>c</sub> =125°C Fig.2	-	-	4.0	ms
Current limit resistor	R <sub>ALM</sub>	Alarm terminal	1425	1500	1575	ohm

● Protection Section ( V<sub>cc</sub>=15V)

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Over Current Protection Level of Inverter circuit	I <sub>oc</sub>	T <sub>j</sub> =125°C	113	-	-	A
Over Current Protection Level of Brake circuit	I <sub>oc</sub>	T <sub>j</sub> =125°C	75	-	-	A
Over Current Protection Delay time	t <sub>DOC</sub>	T <sub>j</sub> =125°C	-	5	-	μs
SC Protection Delay time	t <sub>SC</sub>	T <sub>j</sub> =125°C Fig.4	-	-	8	μs
IGBT Chip Over Heating	T <sub>jOH</sub>	Surface of IGBT chips	150	-	-	°C
Over Heating Protection Hysteresis	T <sub>jH</sub>		-	20	-	°C
Under Voltage Protection Level	V <sub>UV</sub>		11.0	-	12.5	V
Under Voltage Protection Hysteresis	V <sub>H</sub>		0.2	0.5	-	V

● Thermal characteristics( T<sub>c</sub>=25°C)

Item			Symbol	Min.	Typ.	Max.	Unit
Junction to Case thermal resistance *9	Inverter	IGBT	R <sub>th(j-c)</sub>	-	-	0.63	°C/W
		FWD	R <sub>th(j-c)</sub>	-	-	0.855	°C/W
	Brake	IGBT	R <sub>th(j-c)</sub>	-	-	0.63	°C/W
Case to fin thermal resistance with compound			R <sub>th(c-f)</sub>	-	0.05	-	-°C/W

\*9 For 1 device, Case is under the device

● Noise Immunity ( V<sub>DC</sub>=300V, V<sub>cc</sub>=15V, Test Circuit Fig.5)

Item	Condition	Min.	Typ.	Max.	Unit
Common mode rectangular noise	Pulse width 1μs, polarity ±, 10minuets Judge : no over-current, no miss operating	±2.0	-	-	kV
Common mode lightning surge	Rise time 1.2μs, Fall time 50μs Interval 20s, 10 times Judge : no over-current, no miss operating	±5.0	-	-	kV

## ● Recommendable value

Item	Symbol	Min.	Typ.	Max.	Unit
DC Bus Voltage	V <sub>DC</sub>	-	-	400	V
Operating Supply Voltage of Pre-Driver	V <sub>cc</sub>	13.5	15.0	16.5	V
Screw torque (M5)	-	2.5	-	3.0	Nm

## ● Weight

Item	Symbol	Min.	Typ.	Max.	Unit
Weight	W <sub>t</sub>	-	270	-	g

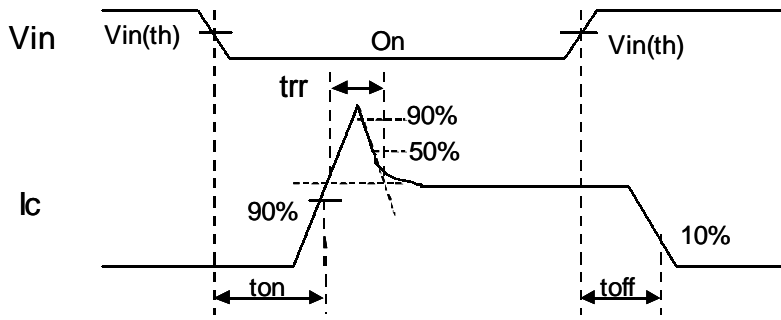


Figure 1. Switching Time Waveform Definitions



Fault : Over-current, Over-heat or Under-voltage

Figure 2. Input/Output Timing Diagram

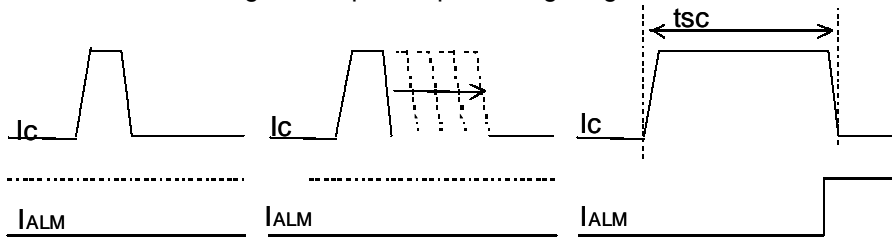


Figure.4 Definition of tsc



Figure 5. Noise Test Circuit

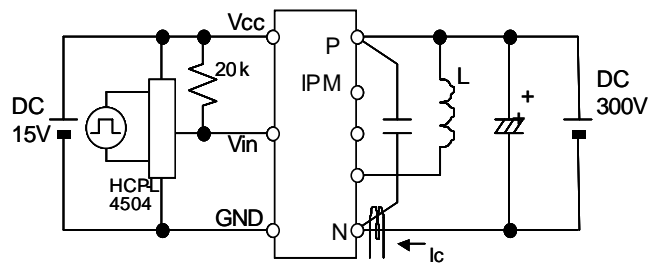


Figure 6. Switching Characteristics Test Circuit

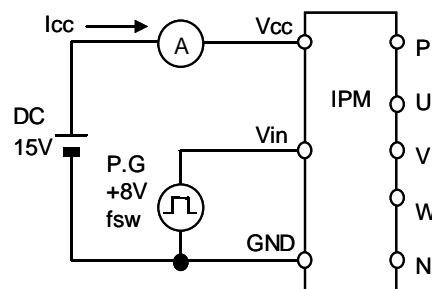
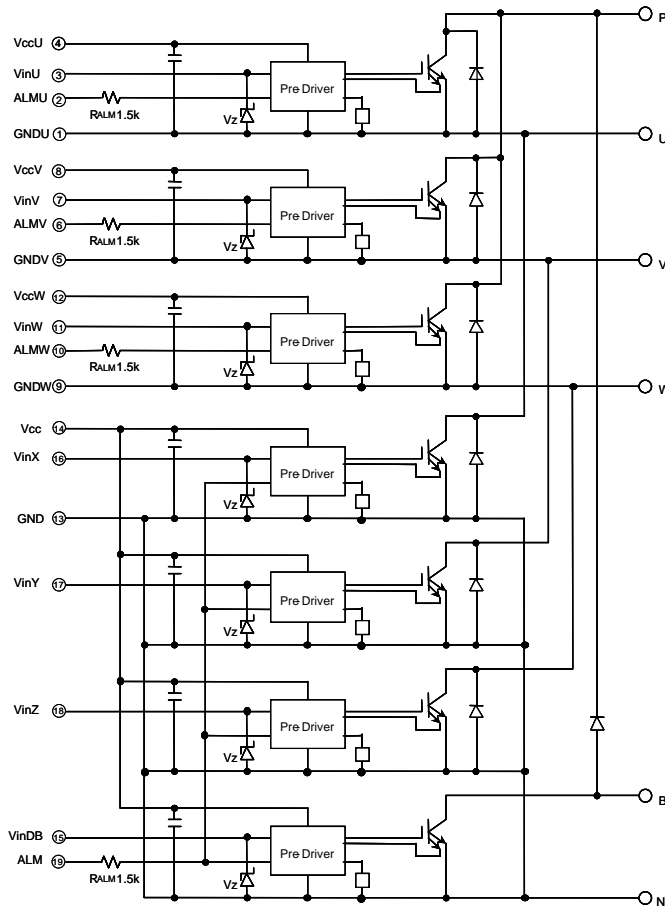


Figure 7. Icc Test Circuit

Block diagram

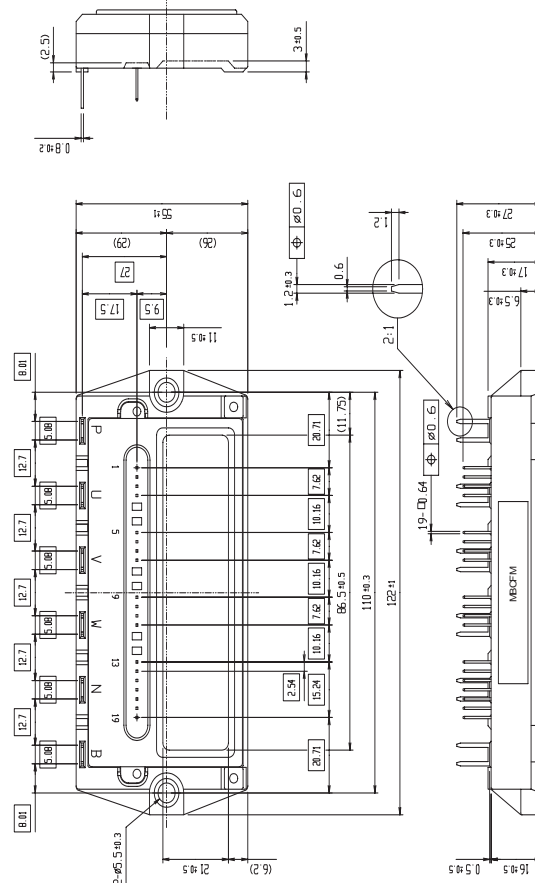


Pre-drivers include following functions

1. Amplifier for driver
2. Short circuit protection
3. Under voltage lockout circuit
4. Over current protection
5. IGBT chip over heating protection

Outline drawings, mm

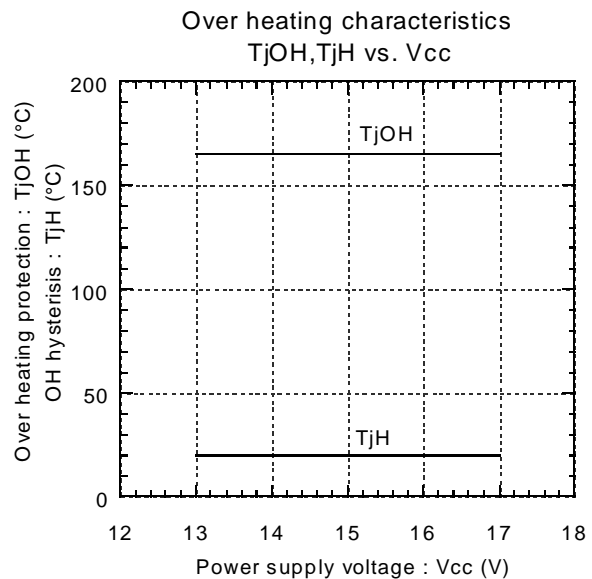
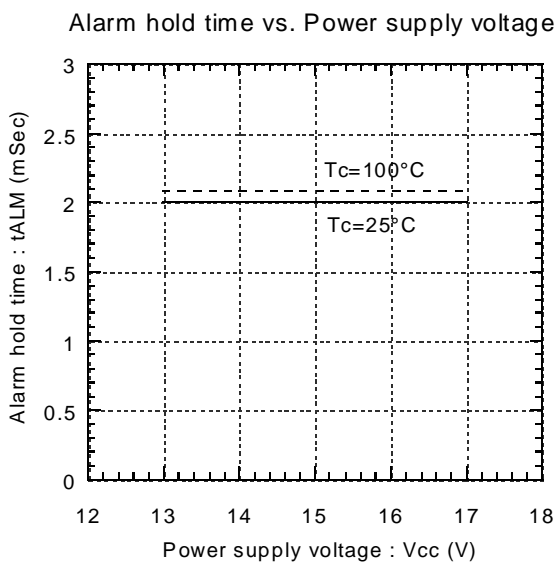
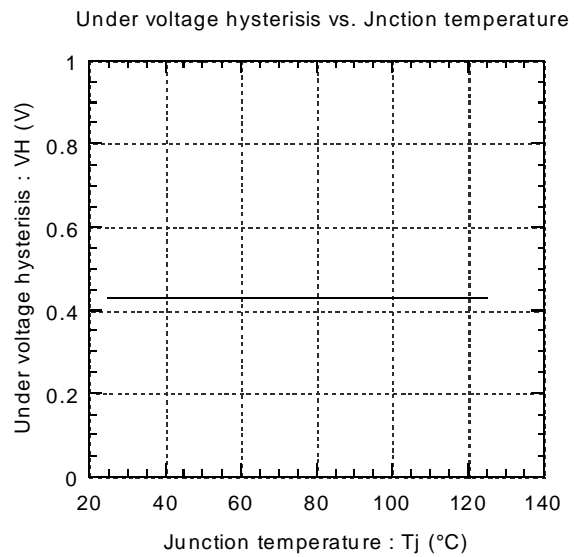
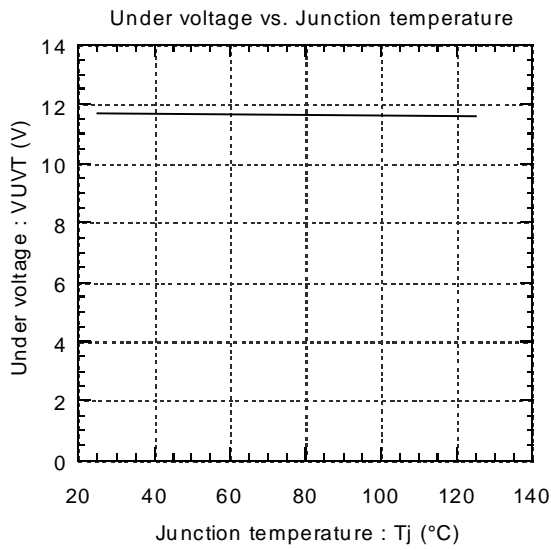
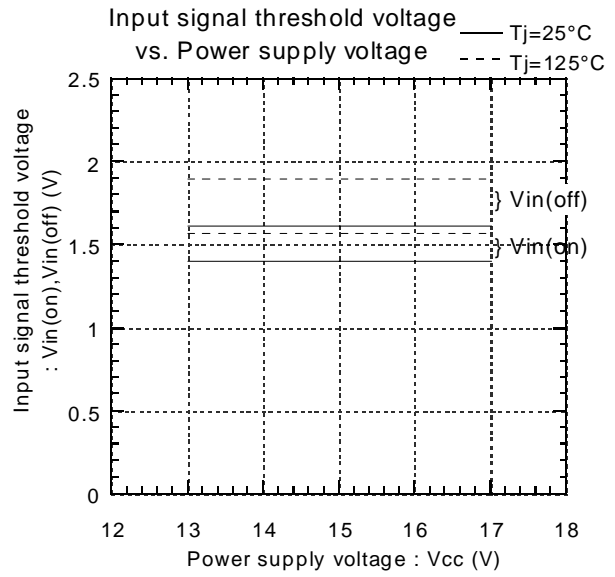
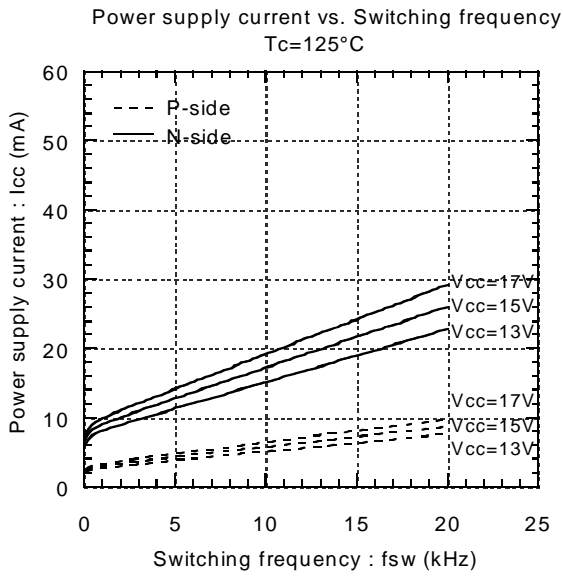
Package Type : P622



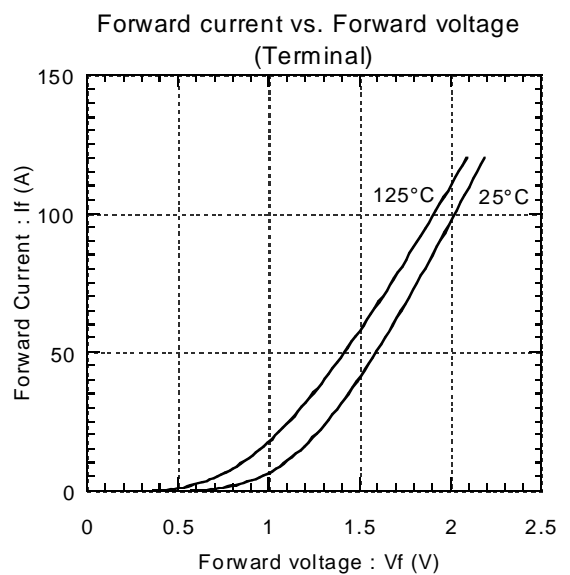
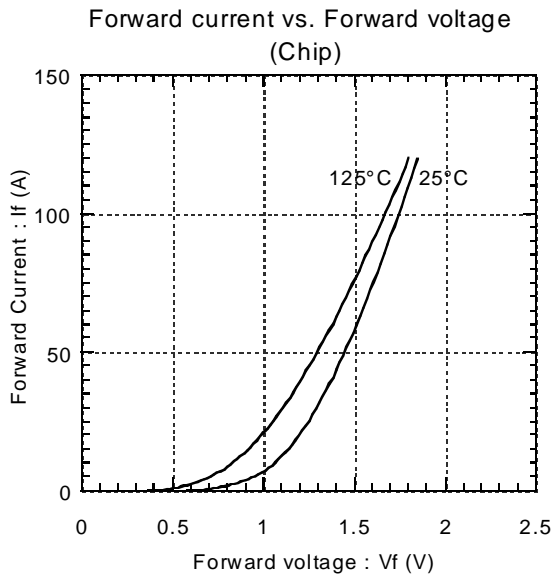
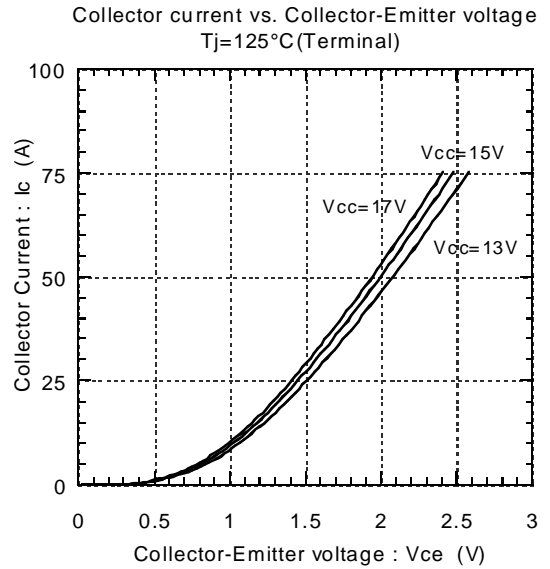
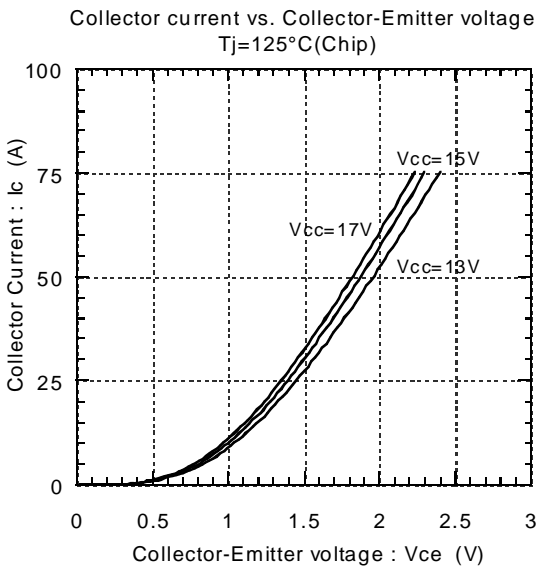
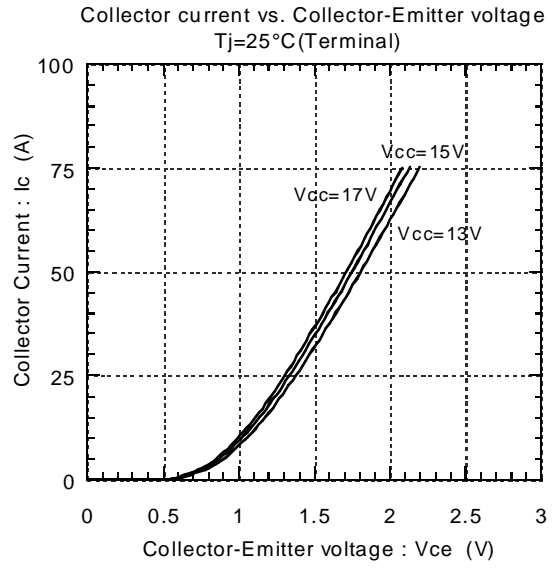
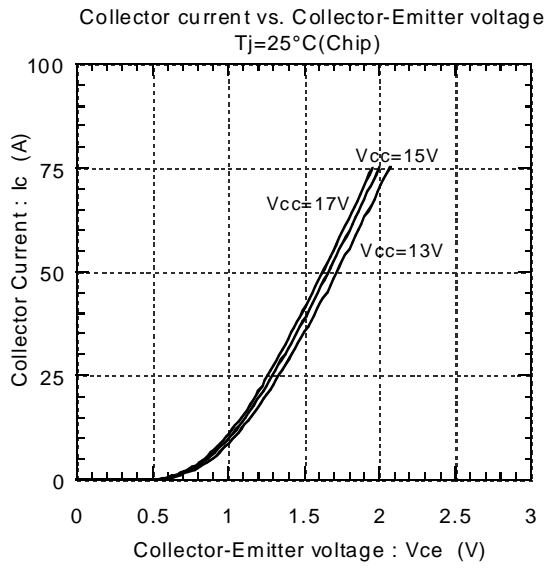
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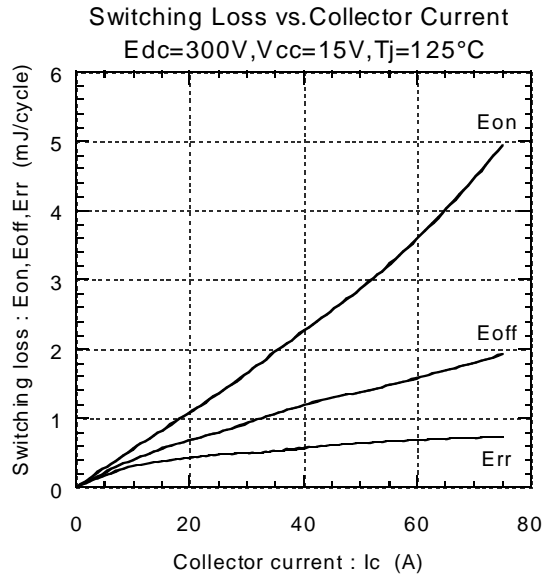
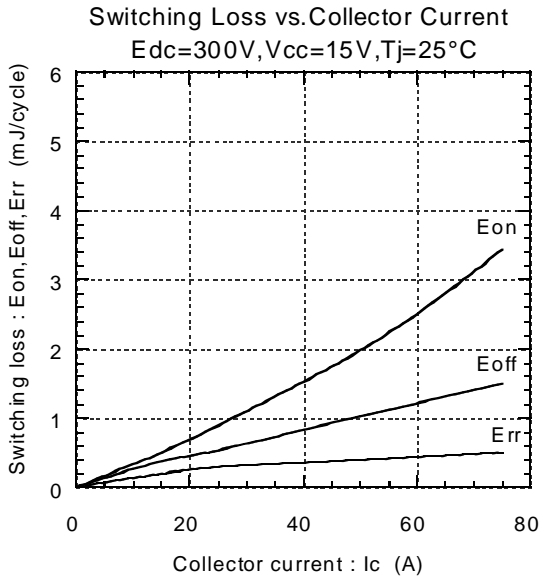
Characteristics

Control circuit characteristics (Representative)

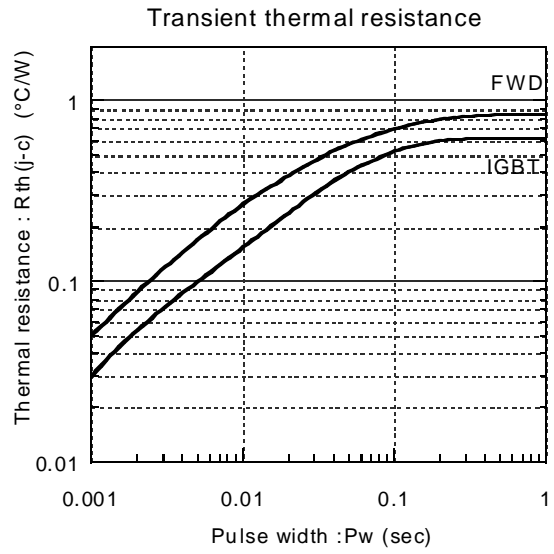
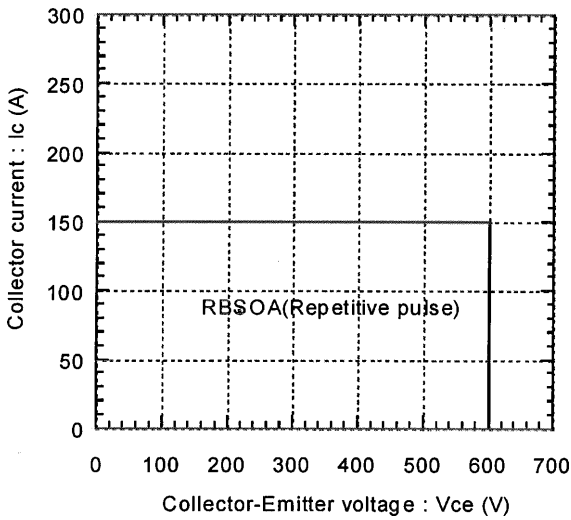


● Main circuit characteristics (Representative)

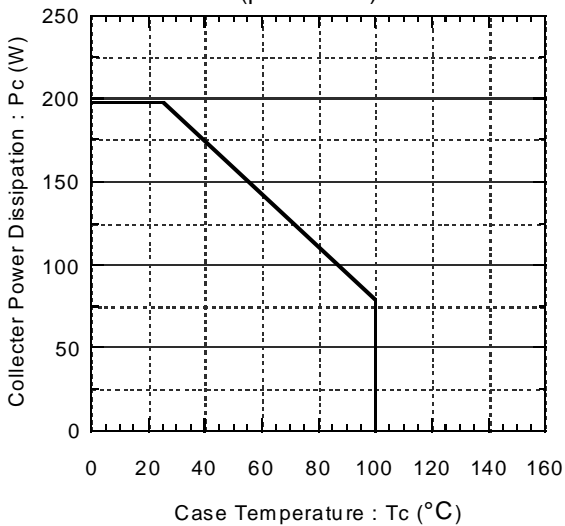




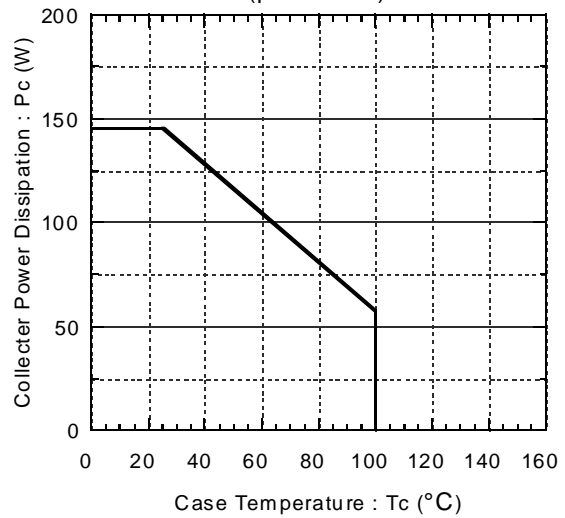
Reversed biased safe operating area  
 $V_{cc}=15V, T_j \le 125^\circ C$

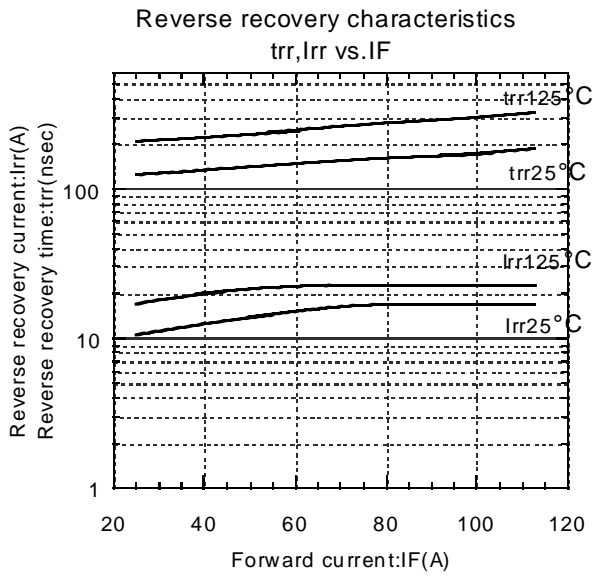
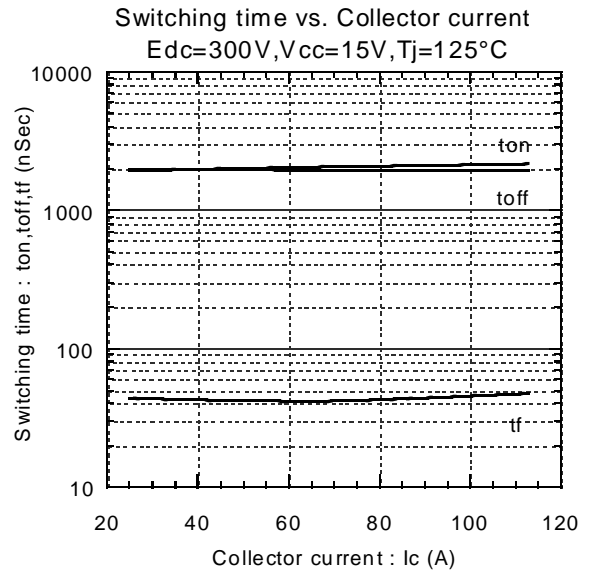
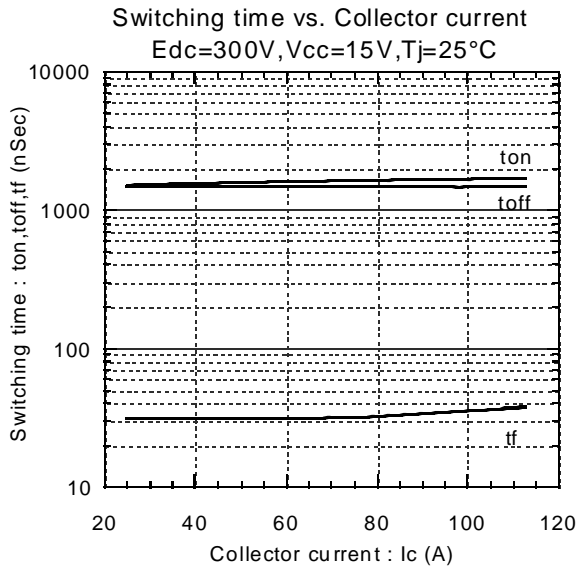


Power derating for IGBT  
(per device)



Power derating for FWD  
(per device)



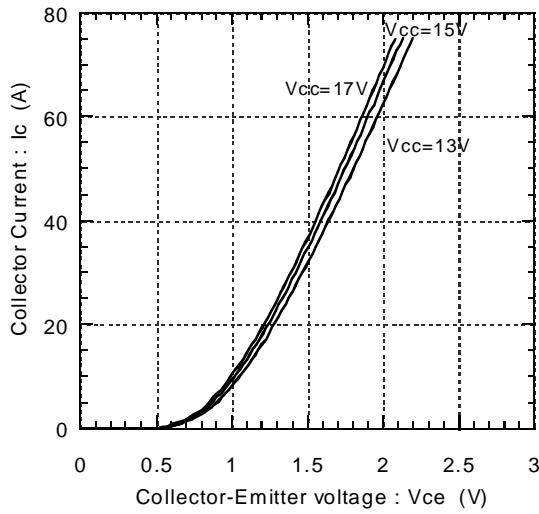




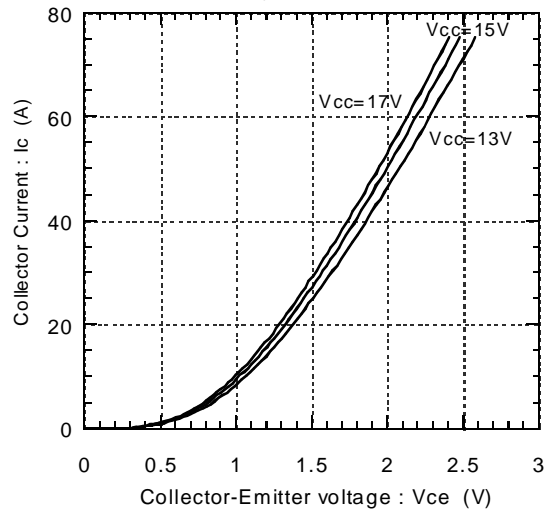
Characteristics

Dynamic Brake Characteristics (Representative)

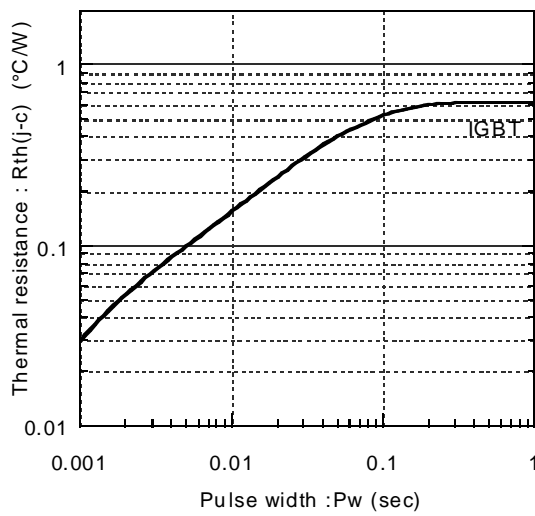
Collector current vs. Collector-Emitter voltage  
T<sub>j</sub>=25°C



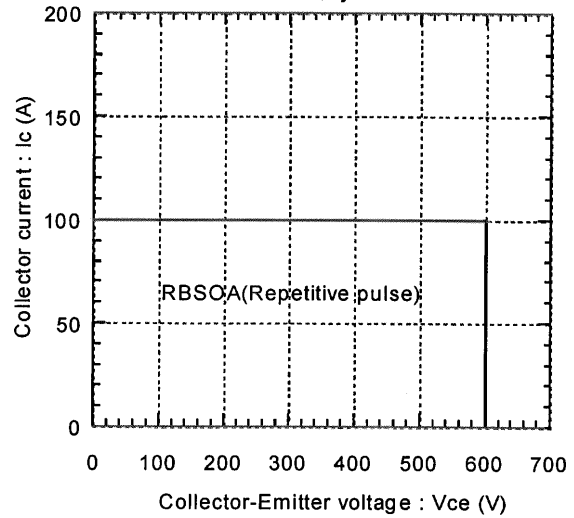
Collector current vs. Collector-Emitter voltage  
T<sub>j</sub>=125°C



Transient thermal resistance



Reversed biased safe operating area  
V<sub>cc</sub>=15V, T<sub>j</sub> ≤ 125°C



Power derating for IGBT  
(per device)

