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SPECIFICATION

Device Name : IGBT module

Type Name : 2MB1150NT-120A

Spec. No. : MS5F4143

Fuji Electric Co., Ltd.
Matsumoto Factory

	DATE	NAME	APPROVED	Fuji Electric Co., Ltd.		
DRAWN	Sep. 19 '97	T. Kobayashi	T. HOSEN	DWG. NO.	MS5F4143	1/8
CHECKED	Sep. 19 '97	S. Ogasawara				

Revised Records

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Fuji Electric Co., Ltd.

DWG. NO.

MS5 F 4143

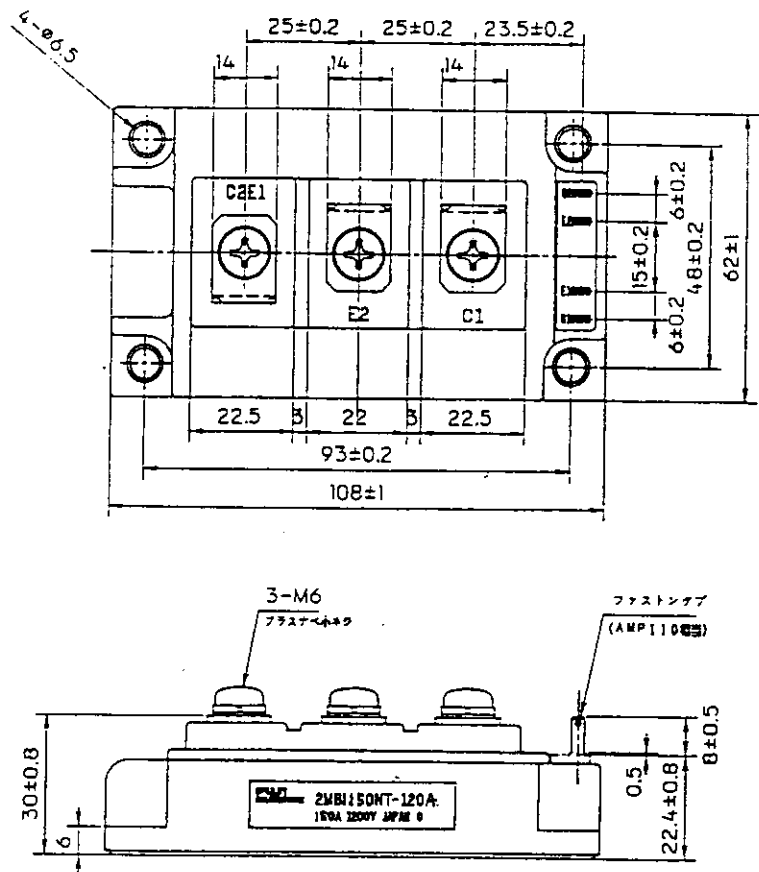
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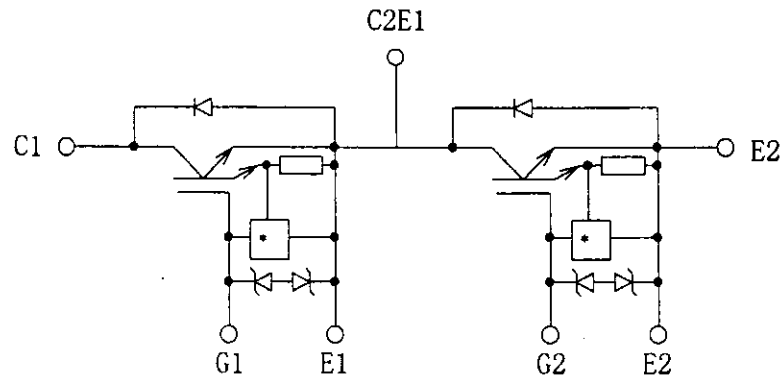
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2MBI150NT-120A

1. Outline Drawing
Unit : mm



2. Equivalent circuit



* NLU (Over Current Limiting Circuit)

3. Absolute Maximum Ratings (at $T_c=25^{\circ}\text{C}$ unless otherwise specified)

Items		Symbols	Ratings	Units
Collector-Emitter voltage		V_{CES}	1200	V
Gate-Emitter voltage		V_{GES}	± 20	V
Collector current	Continuous	I_c	150	A
	1ms	I_c pulse	300	
		$-I_c$	150	
	1ms	$-I_c$ pulse	300	
Max. power dissipation		PC	1210	W
Operating temperature		T_j	+150	$^{\circ}\text{C}$
Storage temperature		T_{stg}	$-40 \sim +125$	$^{\circ}\text{C}$
Isolation voltage		V_{is}	AC 2500 (1min.)	V
Screw torque	Mounting *1		3.5	N · m
	Terminals *2		4.5	

Note : *1 Recommendable value : 2.5~3.5 N · m (M5) or (M6)

*2 Recommendable value : 3.5~4.5 N · m (M6)

4. Electrical characteristics (at $T_j=25^{\circ}\text{C}$ unless otherwise specified)

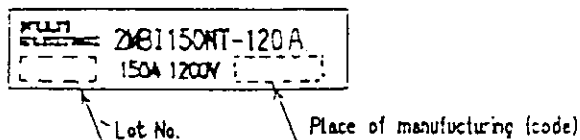
Items	Symbols	Characteristics			Conditions	Units
		min.	typ.	max.		
Zero gate voltage Collector current	I_{CES}			2.0	$V_{GE}=0\text{V}$, $V_{CE}=1200\text{V}$	mA
Gate-Emitter leakage current	I_{GES}			30	$V_{CE}=0\text{V}$, $V_{GE}=\pm 20\text{V}$	μA
Gate-Emitter threshold voltage	$V_{GE(th)}$	4.5		7.5	$V_{CE}=20\text{V}$, $I_c=150\text{mA}$	V
Collector-Emitter saturation voltage	$V_{CE(sat)}$			3.5	$V_{GE}=15\text{V}$, $I_c=150\text{A}$	V
Input capacitance	C_{ies}		27000		$V_{GE}=0\text{V}$	pF
Output capacitance	C_{oes}		12500		$V_{CE}=10\text{V}$	
Reverse transfer capacitance	C_{res}		10000		$f=1\text{MHz}$	
Turn-on time	t_{on}		0.65	1.2	$V_{cc}=600\text{V}$	μs
	t_r		0.25	0.6	$I_c=150\text{A}$	
Turn-off time	t_{off}		0.85	1.5	$V_{GE}=\pm 15\text{V}$	
	t_f		0.35	0.5	$R_G=5.6\Omega$	
Diode forward on voltage	V_F			3.4	$I_F=150\text{A}$, $V_{GE}=0\text{V}$	V
Reverse recovery time	t_{rr}			350	$I_F=150\text{A}$	ns
Short-circuit withstand capability	P_w	10			$V_{cc}=800\text{V}$, $V_{GE}=+15\text{V}$ $R_G=2.7\Omega$	μs

5. Thermal resistance characteristics

Items	Symbols	Characteristics			Conditions	Units
		min.	typ.	max.		
Thermal resistance	$R_{th(j-c)}$			0.10	IGBT	$^{\circ}\text{C/W}$
	$R_{th(j-c)}$			0.24	Diode	
	※		0.025		the base to cooling	
	$R_{th(c-f)}$				fin	

※ This is the value which is defined mounting on the additional cooling fin with thermal compound.

6. Indication on module (モジュール表示)



7. Applicable category (適用範囲)

This specification is applied to IGBT module named 2MB1150NT-120A.
本納入仕様書は、IGBTモジュール 2MB1150NT-120A に適用する。

8. Storage and transportation notes (保管、運搬上の注意事項)

- The IGBT module should be stored at a standard temperature of 5 to 35℃ and humidity of 45 to 75%.
常温保存が望ましい。(5~35℃、45~75%)
- Store modules in a place with few temperature changes in order to avoid condensation on the module surface.
急激な温度変化の無きこと。(モジュール表面が結露しないこと)
- Avoid exposure to corrosive gases and dust.
腐蝕性ガスの発生場所、塵埃の多い場所は避けること。
- Avoid excessive external force on the module.
製品に荷重がかからないように十分注意すること。
- Store modules with unprocessed terminals.
モジュールの端子は未加工の状態で保管すること。
- Do not drop or otherwise shock the modules when transporting.
製品の運搬時に衝撃を与えたり、落下させたりしないこと。

9. Heat sink mounting notes (ヒートシンク取り付け上の注意事項)

- The mounting surface of the heat sink should be finished to a roughness of 10μm or less and a warp between screw holes of 100μm or less.
本モジュールを取り付ける冷却体の取付面の仕上げは、粗さ10μm以下、取付ネジ間で平坦度100μm以下とする。
- Each mounting screw should be fastened using a specified torque after pre-fastening using a 1/3 specified torque.
取付けネジは、規定の1/3のトルクで仮締を行った後、規定のトルクで本締を行って下さい。
- If the above notes are not met, it has a possibility to break the insulation between the IGBT module's chips and metal base.
上記注意事項の範囲外で御適用した場合、IGBTモジュールのチップと金属ベース間の絶縁破壊を生ずる可能性があります。

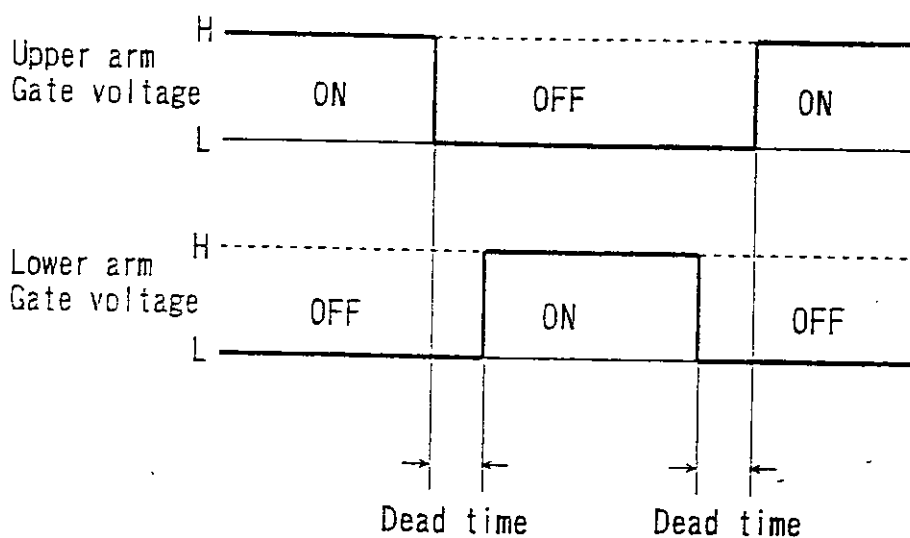
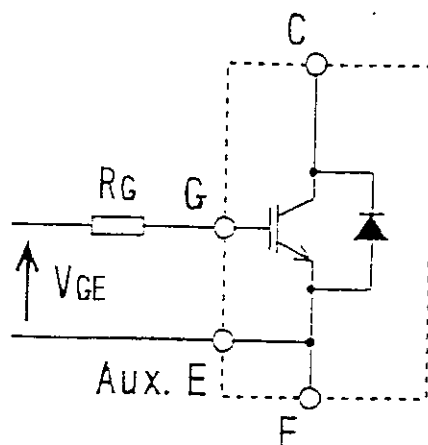
10. Reverse gate bias voltage (ゲート逆バイアス)

- Recommendable value of the reverse gate bias voltage : -7V(typ.), -5V(min.), $RG=5.6\Omega$.
ゲート逆バイアスの推奨値 : -7V(typ.), -5V(min.), $RG=5.6\Omega$
- The reverse gate bias voltage means the voltage between the gate terminal and the auxiliary emitter terminal of the module.
ゲート逆バイアス電圧は、モジュールのゲート端子と補助エミッタ端子間の電圧である。

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11. Setting dead-time (デッドタイムの設定)

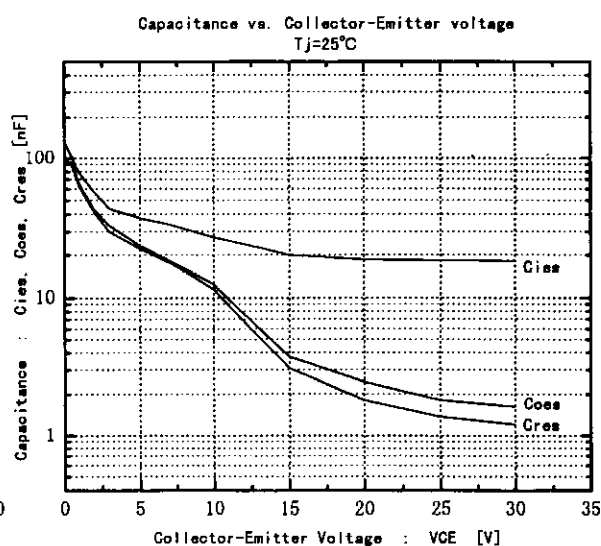
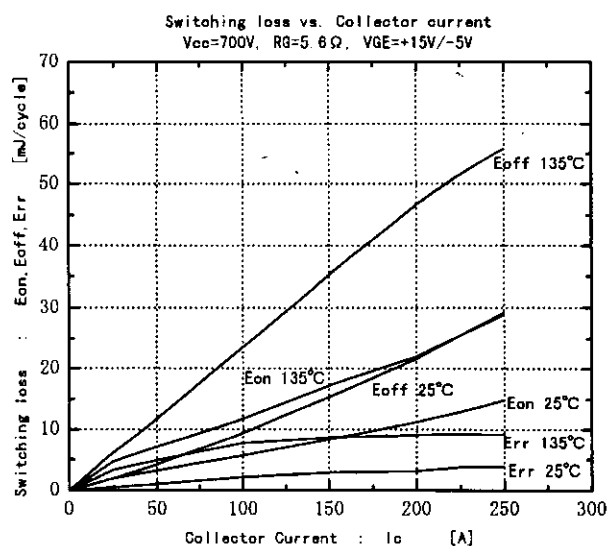
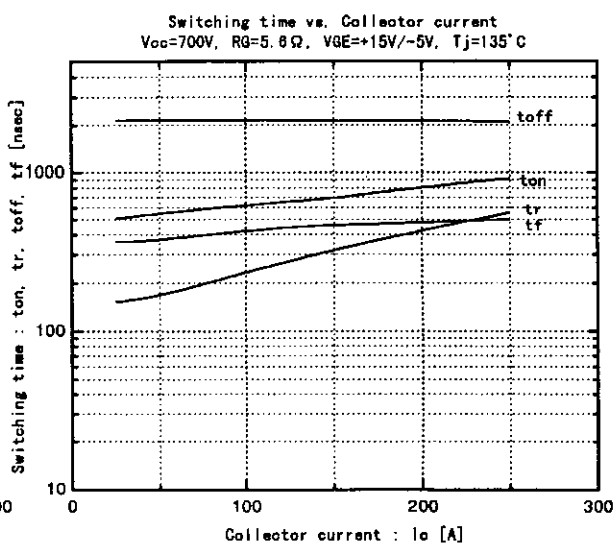
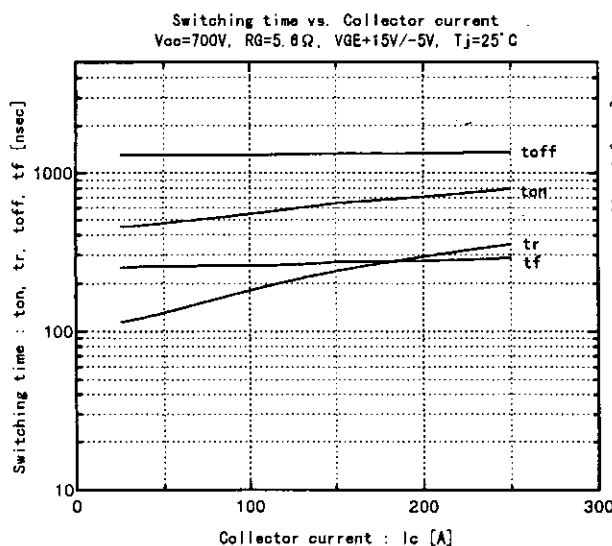
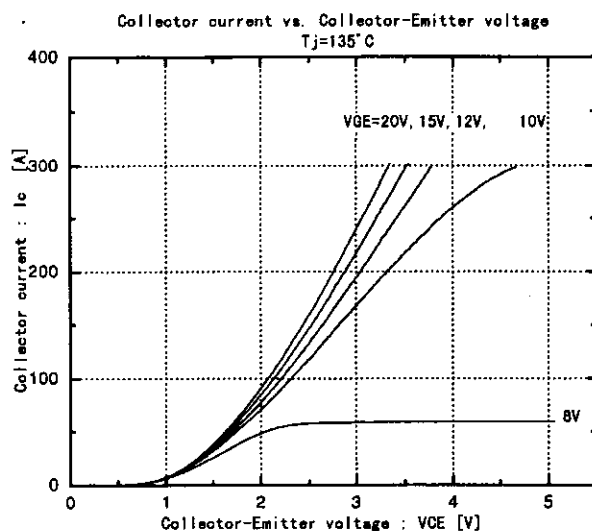
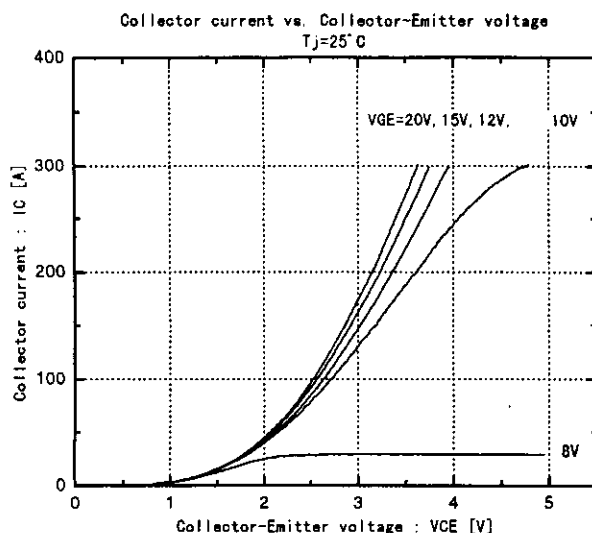
- Recommendable value of the dead-time : $3\mu\text{s}$ (min)
デッドタイムの推奨値 : $3\mu\text{s}$ (min)



Dead time timing chart

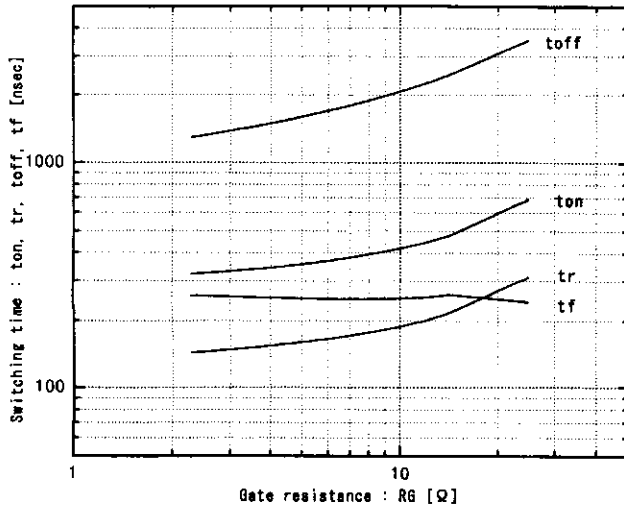
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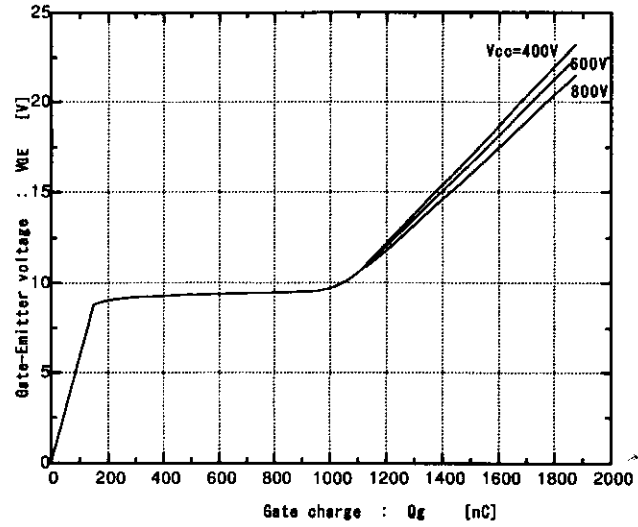


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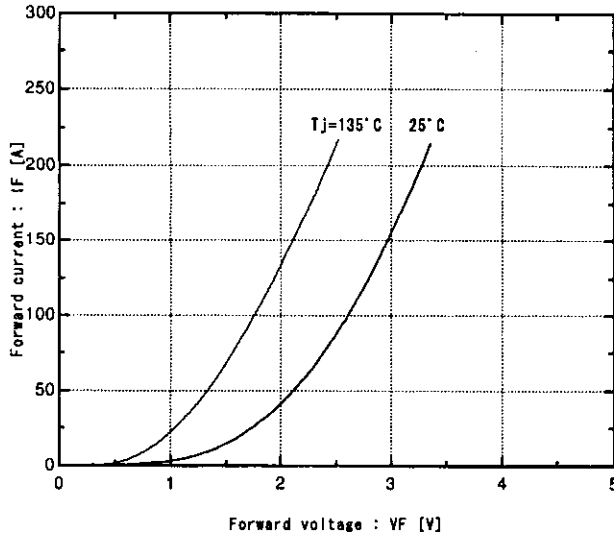
Switching time vs. R_G
 $V_{CC}=700V$, $I_C=150A$, $V_{GE}=+15V/-5V$, $T_J=25^\circ C$



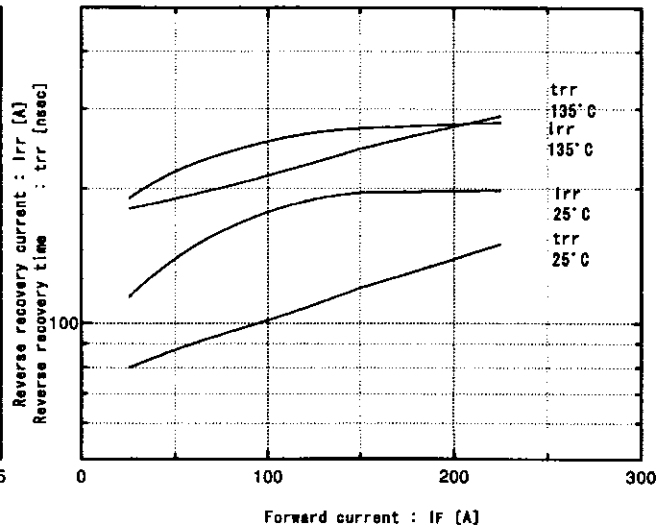
Dynamic input characteristics
 $T_J=25^\circ C$



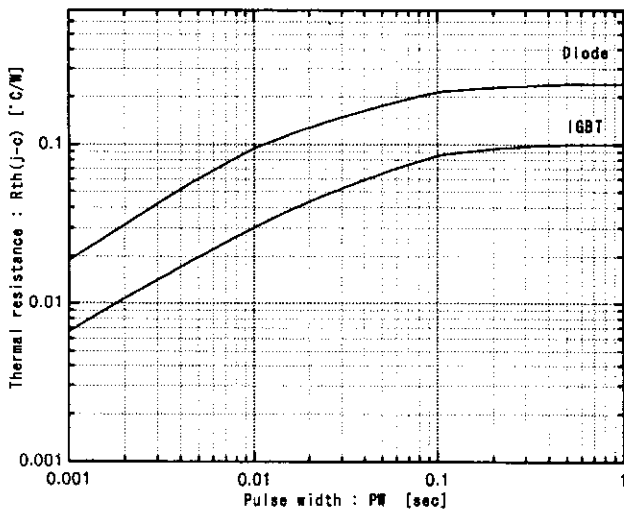
Forward current vs. Forward voltage
 $V_{GE}=0V$



Reverse recovery characteristics
 t_{rr} , I_{rr} vs. I_F



Transient thermal resistance



Reversed biased safe operating area
 $+V_{GE}=15V$, $-V_{GE}\leq 15V$, $T_J\leq 135^\circ C$

